On the Scalability of Constraint Solving for Static/Offline Real-Time Scheduling

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Outline

Motivation

Generalities Our objective Running example

Modeling and solving

General encoding rules Test cases Results scheduling problem resource heterogeneity system load pipelining

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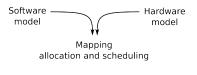


Figure: Y-chart design methodology

Allocating and scheduling tasks and messages over a network of resources is NP-Complete (for most of the problem instances).

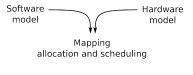


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Two main approaches:

- Avoiding NP-Completeness: heuristics
- Accepting it: general solvers (Integer Linear Programming / SAT Modulo Theory / Constraint Programming)

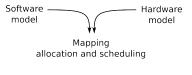


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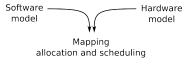


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- Avoiding NP-Completeness: heuristics
- Accepting it: general solvers (Integer Linear Programming / SAT Modulo Theory / Constraint Programming)
- How far? (with "up to date" solvers)
 - Small size problems (can still be interesting)
 - Design for scalability

- SAT solvers can handle large problems functional model checking (time model checking?)
- It has practical interests in static scheduling (meta-programming) and compilation problems.

We need an empirical evaluation of static distributed real-time scheduling problems. When can we apply exact solving techniques?

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Contributions

- Encoding of a variety of scheduling problems as SMT/ILP/CP problems
- Synthetic and realistic test cases
- Empirical hardness related to various parameters (problem size, preemtiveness, satisfiability or optimization target, system load, dependencies, pipelining, resources homogeneity, unique or multiple task periods ...).

Two real-life test cases (FFT and Platooning applications) and synthetic benchmarks.

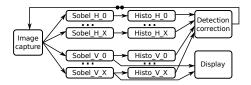


Figure: Platooning dataflow graph application

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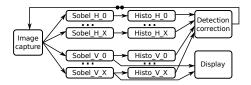


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Reduces traffic jams (and fuel consumption).

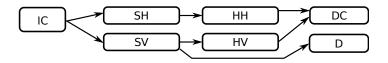


Figure: Platooning dataflow graph application, no pipelining, no data-parallelism (X=0) and simplified labels...

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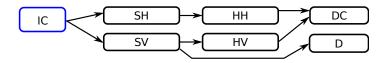


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Application constraints:

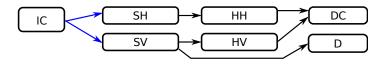


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Application constraints:

- ► IC_{Stop} = IC_{Start} + IC_{Duration}
- $SH_{Start} \ge IC_{Stop}$
- ► SV_{Start} ≥ IC_{Stop}

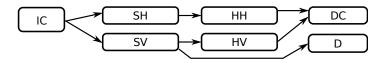
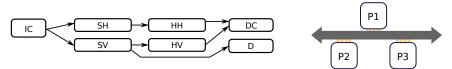


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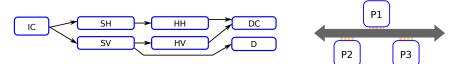
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Figure: Platooning dataflow graph application single period, no data-parallelism (X=0) and simplified labels...



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Resource constraints:

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$$IC_{Map} \in \{P1, P2, P3\}, SH_{Map} \in \{P1, P2, P3\}, ...$$

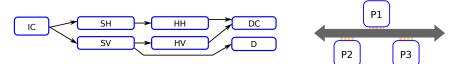


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Resource constraints:

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$$IC_{Map} \in \{P1, P2, P3\}, SH_{Map} \in \{P1, P2, P3\}, ...$$

 $SH_{Map} == SV_{Map} \rightarrow SH_{Start} \ge SV_{Stop} \oplus SH_{Stop} \ge SV_{Start}$
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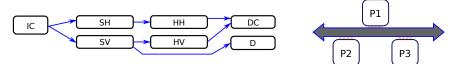


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Resource constraints:

The encoding is a set of linear constraints and a set of decision variables.

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Realistic modeling: We also associated a time with messages (communication between tasks) with the following rules:

- Messages cannot overlap (because of the single Bus)
- Messages have no cost if the source task and destination tasks are allocated on the same resource (shared memory, you only need to pass a pointer, disputable)

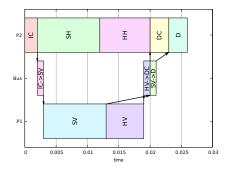


Figure: Gantt diagram (optimal non-preemptive schedule)

in plain English

- 1. Each task is allocated on exactly one processor
- 2. If two tasks are ordered, the second starts after the first ends
- 3. If two tasks are allocated on the same processor, they must be ordered
- 4. The source and destination of a dependency must be ordered
- 5. The bus communication associated with a dependency (if any) must start after the source task ends and must end before the destination task starts
- 6. When two dependencies require both a bus communication, these communications must be ordered
- 7. If two dependencies are ordered, the first must end before the second starts

8. All tasks must end at a date smaller or equal than the schedule length

9. ...

We look at:

- Average solving time
- Timeout (1 hour) ratio

When we vary:

Scheduling problem single or multi period, preemptive or not, resource homogeneity

Objective schedulability analysis vs optimization Average system loads 25%, 75%, 87.5%,125% Pipelining depth

Onto:

- Synthetic test cases
- Two realistic test cases (FFT and Platooning applications)

Why merge the two ?

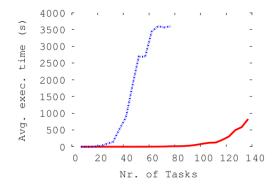


Figure: Single Period Non-Preemptive vs Multi Period Preemptive

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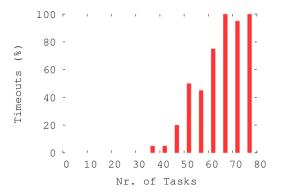


Figure: Timeout (1h) rate for preemptive multi-periodic

→ Preemptive problems are of greater complexity → Non-preemptive scheduling is actually one of the first motivations of this work.

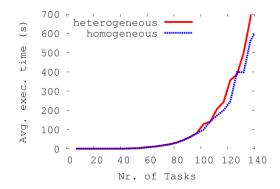


Figure: Homogeneous resources VS Heterogeneous resources

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 \rightarrow Not so clear

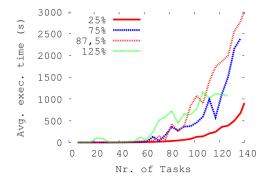


Figure: System load

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 \rightarrow Underloaded and overloaded systems are easier to handle.

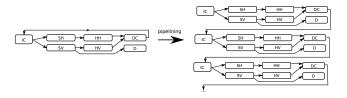


Figure: Pipelining: Cyclic dataflow graph to DAG

Pipelining raises the depth of the graph

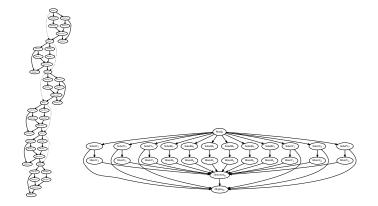


Figure: "Aspect" of the graph

 \rightarrow Deep graphs will scale much better than large graphs

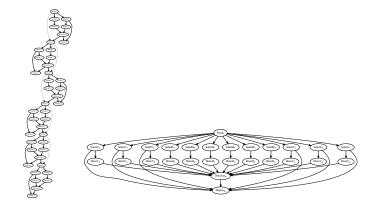


Figure: "Aspect" of the graph

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 \rightarrow Deep graphs will scale much better than large graphs Even with symmetry breaking techniques

Good practices for encoding scheduling problems:

- The order of the variables matters: somehow ask the solver to select the variables (eq. the tasks) in the topological order of the tasks, beginning with the small values of the variable state.
- Non-synthetic graphs can exhibit a lot of symmetry (e.g. split merge graph). Associating variables (*T_{Map}* and *T_{Start}*) to each task of a split/merge is certainly not scalable → Symmetry breaking techniques
- Symmetries can also occur in architecture graph (if homogeneous resources) → Symmetry breaking techniques (cumulative scheduling).

Thank you

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 ${\sf Questions}\ ?$