

Esterel Language Quick Reference Card Revision 3.0

Convertiens						
bold	Estate like word	Conventions				
	Alternative	LI Optional term ::= Expansion term				
0	Repeatable	Introduces one sequential control element				
1	Introduces a sequential control element that can be easily removed by entimization					
Inggont						
[asser]	2 sig_id_ = exp_if_boo	Valued signal				
[Hexc]		Statements stat:-				
{ stat }		Bracketed statement to unambiguously express control flow				
nothin	a	Empty statement				
_		Terminates instantaneously				
pause		 Unit delay – pause for one instant 				
halt		Never terminates				
		Can only be aborted				
open {port-Id,} in		Makes port to components visible in current scope				
stat end [open]						
loc := exp		Assigns the evaluation of an expression to a variable				
	,	location, loc is for example X, Y[2],				
ap11 n	roc id ([proc.prg.])	Assignment is instantaneous Instantaneous				
call p	roc-lu((proc-arg,))	Terminates instantaneously				
{eau	ation.	Concurrent or sequenced execution of equations				
[}]		• Equation blocks can be replicated using the for dopar				
avatoj	n [acal [now+] [[]	statement				
Sustai {equi	ation.	Equations same as <i>emit</i>)×			
[}]		Acts as a macro for:				
		loop emit {equation } : pause				
		end loop				
stat ₁ ;	stat ₂	Sequence statement				
1000		stat2 immediately starts as soon as stat1 terminates Infinite loop: immediately restarts its body when				
stat		terminated				
end [loop]		 Never terminates (can be aborted or <i>exit</i>-ed) 				
alwaya		stat must not be instantaneous (combinational)	2			
stat		permanent one	2 ^ ^.			
end [always]		stat should be instantaneous.				
[nogiti	ivol	Finite loop: positive keyword asserts that the body will				
repeat	[count-id:=] exp	be executed at least once				
times	locant rate 1 oub	 count-id = exp down to 1. 				
stat		count-ia is read-only				
end [re	epeat]					
if boo	l-exp	Branching according to the value of test expression				
[the:	n Stat					
[els	e STAT ₂ e					
if	L]	Multiple branching tests: the first true signal expression in				
{cas	e bool-exp do	the list starts its statement				
stat						
[def	ault do <i>stat</i>					
end [if	£]					
stat ₁ stat ₂		Parallel statements				
		 Lasts as long as one thread is still alive 				

for iter-id < static-exp dopar	Replication statement: generates <i>static-exp</i> copies of <i>statibat</i> run concurrently	
stat	 <i>iter-id</i> iterator goes from 0 to <i>static-exp-1</i> and is 	
	read-only	
or iter-id	Static-exp must be statically evaluable Same as above except that <i>iter-id</i> goes from <i>static</i> -	
.n [static-exp1 static-exp2]	exp1 to static-exp2	
lopar		
stat		
and [for]	Terminates when guard expression is true	
wait guard-exp	Instantaneous with <i>immediate</i> keyword	-
	Macro for: abort halt when guard-exp	
wait guard-exp do	 Starts stat when the guard expression is true Is equivalent to: 	-
and [await]	await guard-exp; stat	
wait	Multiple waiting; the first guard expression in the list	
{case guard-exp do stat}	that is true starts its statements	
end [await]		(1)
weak abort	 Abortion; kills the statement when the guard expression is true 	(I) 1241
then quard-exp do	The weak variant starts the statement in the current	. *
stat	instant when the guard expression is true, before killing it	
and [abort]]	Can be instantaneous with the <i>immediate</i> keyword	
weak] abort	Multiple case abortion (strong or weak); the first guard	(1)
stat		245
{ case quard-exp do { stat } }		
end [abort]		
weak] abort	 stat₁ and stat₂ are concurrently started; stat₁ is observed when a tet terminates 	(1)
stat ₁	 Immediately terminates if <i>stat₂</i> immediately 	24.
stat ₂	terminates	
and [abort]		
oop	Temporal loop with body initially started.	\mathbf{X}
Stat	guard-exp cannot be inimediate Is a macro for:	
acii guara-exp	loop	
	stat; halt	
	when guard-exp	
every quard-exp do	Temporal loop with initial waiting of true guard	2.1
stat	expression	M. 4.
end [every]	 guard-exp can be immediate Macro for: await guard-exp: 	
	loop	
	stat each guard-exp	
weak] suspend	Freezes the execution of a statement when guard	
stat	expression is true. The weak variant keeps the combinational part running	
nen DOOI-exp	Mechanism to catch exceptions raised by the body	
stat	statement	
handle <i>except-evt</i> do		
stat}		
end [trap]		
exit except-evt	 Raises an exception 	

(1) Only weak abortion may introduce sequential element

<pre>finalize stat stat with stat end [finalize] signal {sig-id [([exp])] [temp reg[1]][[combine] [init exp], } {port sig-id : [input output]: {extends inpu unit-id,} [[refinement-clau stat</pre>	: value] type inputoutput] intf-unit-id, } t output inputoutput] intf- Se,}] in	 Starts stat₂ as soon as stat₁ spontaneously terminates or is strongly or weakly aborted stat₂ must be instantaneous Local declaration of a signal Determines its scope See module-header expansion for refinement-clause 		
end [signal]				
oracle { <i>sig-id</i> [: <i>type</i>],} in <i>stat</i> end oracle		Local declaration of signal introducing model non-determinism Determines its scope		
<pre>var {var-id:[temp type[:= exp], in stat</pre>	Local declaration of a variable Determines its scope Cannot be shared between threads			
end [var]				
<pre>mcrun run //nSt-ld/ [[weak] reset [clock sig-id;] n [constant {new [type {new/old [function {new [procedure {new [[signal] {new]]</pre>	 Creates an instance of a module or a multi-clock uit with renaming of the interface elements, i.e., connects the handled current objects with the interface objects of the module <i>new</i> can be an expression bracketed into parenthesis for input renaming.* new can be empty for signal renaming 			
	Predefined Types			
bool	Literals are: true = '1, false = '0			
clock unsigned <n> unsigned<[n]> unsigned unsigned<></n>	Clock type Denotes unsigned numbers from 0 to N-1 Is equivalent to <i>unsigned<2"n ></i> Is equivalent to <i>unsigned<[32] ></i> Only for constant definitions, unsigned<> = N is equivalent to unsigned <n+1> = N Literals can be written in dec, bin, oct or hex</n+1>			
<pre>signed<n> signed<[n] > signed signed<> integer</n></pre>	Denotes signed numbers from: -2 ⁿ⁻¹ to 2 ⁿ⁻¹ -1 Is equivalent to <i>signed</i> -(² 'n > Is equivalent to <i>signed</i> -(³ 2] > Only for constant definitions. signed<> = N is equivalent to signed <n+1> = N is equivalent to <i>signed</i></n+1>			
float	Signed literals are unsigned literals preceded by + or – for example: -123 or +0x10fa Literals examples: 2.31f12f			
double	Literals examples: 2.31, 1.2E-1			
string	string Literal example: "a \"string\"			
	Array Types			
type {[evn]]	Examples: int[5] denotes an array of 5 intege	rs, bool[4][6] denotes a 4*6 matrix of		
cype (lexp)	booleans			
bool [n] Denotes a bilvector				

Expressions							
guard-exp::= [immediate uns-exp times] bool-exp							
encoding-exp ::= u2b:	in(uns-exp, bit-size)						
u2code <my-cod> (uns-exp, uns-size, bit-size)</my-cod>							
u2gray(<i>uns-exp, bit-size</i>)							
u2onehot (uns-exp, bit-size)							
s2bin(sign-exp, bit-size)							
lecoding-exp ::= bin2u (bit-vector, uns-size)							
bin							
code	e2u< <i>my-cod>(bit-vect-exp,uns-size,</i>	bit-size)					
gray2u (bit-vector, uns-size)							
onel	hot2u(bit-vector, uns-size)						
predefined-sia ::= tick never							
Operators							
-	Unary minus	Num					
-	Subtraction	Num					
#	Incompatibility	Boolean					
*	Multiplication	Num					
**	Power	Num					
/	Division	Num					
[op]	Array operator	Extension of operator t					
		array level, examples:					
+	Unary plus	Num					
+	Addition	Num					
<	Less than	Num					
<=	Less than or equal to	Num					
<=>	Equivalence	Boolean					
<>	Inequality	Any					
=	Equality	Any					
=>	Implication	Boolean					
>	Greater than	Num					
>=	Greater than or equal to	Num					
>> <<	Right shift and left shift	Bitvector					
>>> <<<	Signed right shift and signed left shift	Bitvector					
abs	Absolute value	Signed					
and	Conjunction	Boolean					
assert <n></n>	Size assertion	Signed or unsigned					
assert_unsigned <n></n>	Size and sign assertion	Signed or unsigned					
binsize(n)	Number of bits to encode in in binary	Signed of unsigned					
extend	msh and lsh concatonation	Bitvoctor					
mod	Module	Unsigned					
	Multiplexor	Any					
next	Next status of registered signal	Signal status					
next (?sig-id)	Next value of a registered valued signal	Signal value					
not	Negation	Boolean					
or	Disjunction	Boolean					
pre	Pre status of standard signal, false at initial instant	Signal status					
pre (?sia-id)	Previous value of a standard valued signal	Signal value					
prel	Pre status of standard signal, true at initial instant	Signal status					
reverse	Reverses argument bits	Bitvector					
sat <n></n>	Saturation	Signed or unsigned					
sextend	Signed extension of bit vector to nbit	Bitvector					
trunc<[n]>	Truncation	Signed or unsigned					
xor	Exclusive or	Boolean					
	Data						
Unit							
data data-unit-id :		Data unit definition					
data-elem							
end data							

Declaration (data elem :)				
[host generic] two-id two-id-coum				
[host generic] (ype-id type-id=endit() (ype;				
host generic function fct-id ([{type}]]) · type				
host generic procedure proc-id([[in out inout 1/	ne \1) •			
extends [data] unit-id.				
man [man-id] tune-id [[field.id[own] own]] }}				
Interface				
UIIII	Interface unit definition			
Interface Intr-Unit-Id :				
interface-elemi				
end interface				
Declaration (interface-elem::=)				
input Sigid [[[exp]]] [:[temp][value] type				
[init exp][combine function]]:				
output Sig-id [[exp]]] [:[temp] reg[]]][value] type				
[init exp] [combine function]];				
<pre>inputoutput sig-id [{[exp]}] [:[temp][value] type</pre>				
[init exp] [combine function]];				
ortonda [interface data] [mirror] [input]output]in	nutoutoutl unit			
id;				
<pre>input output relation [rel-id:] combinational-sig-exp</pre>	;			
Module				
Unit				
[main] module mod-unit-id : Module unit definition				
module-header				
stat				
end module				
Header (module-neader::=)				
data-elem				
[Interface-eleni]	unction].11			
Multi clock Upit				
Multi-CIOCK Unit				
[main] multiclock mod-unit-id:				
stat (reduced set of statements)				
end multiclock				
Lexical Elements				
id:=\\letter[underline] alphanumeric}				
unsigned-literal $= 0$ b l o $ \mathbf{x} $ hexint with optional ' ' characters				
double-literal:= integer [_ integer] E + - integer				
double-literal"= integer integer K + - integer				
float-literal::= Integer . Integer E + - Integer float-literal::=double-literal E				
float-literal::=double-literal F string-literal ::="a \" double guote\n"				
double-interal::=(integer] .integer] E + - integer] float-literal::=double-literal F string-literal ::='a \ndouble quote\n'' bool-literal ::='a \ndouble quote\n''				
double-literal::= integer] integer] integer] float-literal::= double-literal F string-literal ::= "a " double quote\n" bool-literal ::= "a " double quote\n" bivector-literal::= "b x bexint with optional ' characters				
double-literal::= integer] i.integer] [k]+ -]integer] float-literal::=double-literal F string-literal ::= "a " double quote\n" bool-literal ::= "0 "1 bitvector-literal::= "[b x] hexint with optional '_' characters abel := operator delayed statement@idlstring-literal				
double-literal::= Integer] i. Integer] float-literal::= double-literal F string-literal ::= "a " double quote\n" bool-literal ::= "0 '1 bitvector-literal::= "b x] hexint with optional '_' characters label ::= operator delayed_statement@id string-literal comment ::= / / mono line comment				
Gouble-literal::= integer] i. integer] ii i - integer] float-literal::= ouble-literal F string-literal ::= 'a '' double quote\n" bool-literal ::= 'b '1 bitvector-literal::= 'b x] hexint with optional '_' characters label ::= operator delayed_statement @id string-literal comment ::= // mono line comment /* multi line comment * /				
double-literal::= Integer] i. Integer] float-literal::= double-literal F string-literal ::= "a '" double quote\n" bool-literal ::= '0 '1 bitvector-literal:= `[b x] hexint with optional '_' characters label ::= operator delayed_statement@id string-literal comment ::= // mono line comment /* multi line comments */ // / mono line propagated comments				
double-literal::= Integer] i. Integer] float-literal::= double-literal F string-literal ::= "a \" double quote\n" bool-literal ::= '0 '1 bitvector-literal:= `[b x] hexint with optional '_' characters label ::= operator delayed_statement@id string-literal comment ::= // mono line comment /* multi line comments */ // / mono line propagated comments */				



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FOR ESTEREL STUDIO 5.3

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