

Timed-pNets Semantic Model

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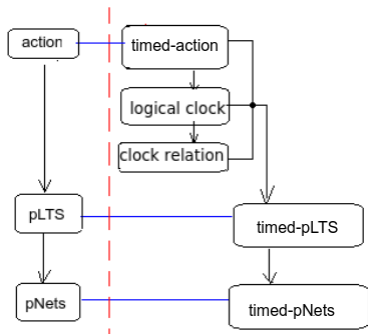
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- Motivation
- Basic definition: Logical clock, clock relations, timed specification
- Open/Close timed-pNets and the Compatibility
- Timed-pNets Tree
- Generate timed specification
- Conclusion and future work

Extend pNets by adding multi logical clocks and clock relations such that:

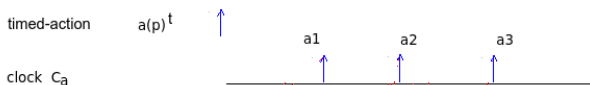
- build a timed model for the distributed systems that have no global physical clock.
- synchronous + asynchronous communications

Our proposal: Timed-pNets

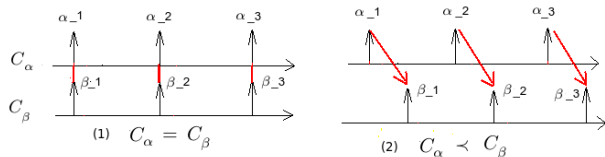


- Logical clocks and clock relations are embedded into timed-pLTS.
- Timed-pNets are built in terms of the communication between timed-pLTSs.
- These communications are represented by clock relations.

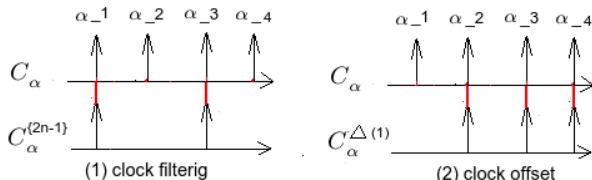
New proposal: logical clock and clock relations



- build logical clock: a sequence of timed-action occurrences
- add constraints between action occurrences

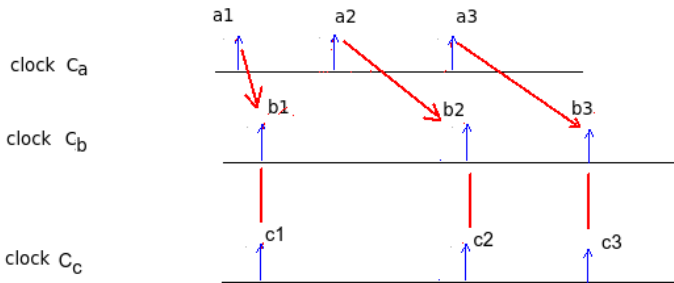


- clock filter and clock offset



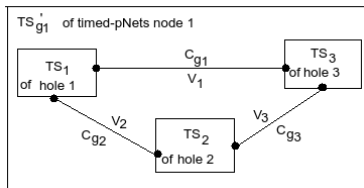
Timed Specification

Timed specification: a set of clocks with clock relations.

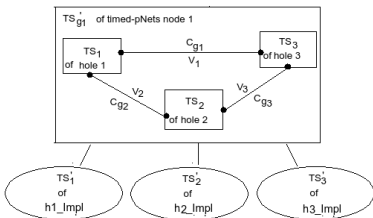


timed specification: $TS = \langle \{C_a, C_b, C_c\}, \{C_a < C_b, C_b = C_c \dots\} \rangle$

Open/Close Timed-pNets node and the compatibility



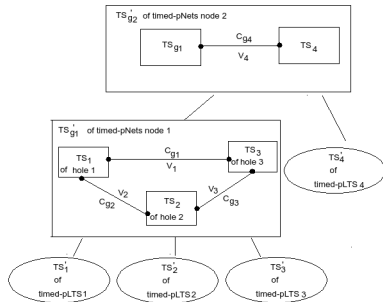
(b) Open Timed-pNet node



(c) Closed Timed-pNets node

Compatibility: $H_Impl \sqsubseteq H \Leftrightarrow TS \ll TS'$.

Timed-pNets Tree Structure

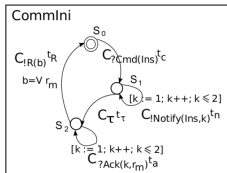


hierarchical model

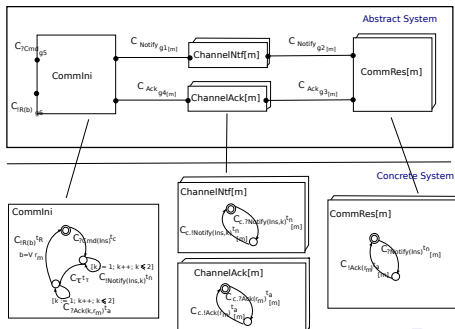
- leaves are timed-pLTS,
- nodes are synchronisation devices
- holes are the timed specifications of the subsystems

Generate Timed Specification

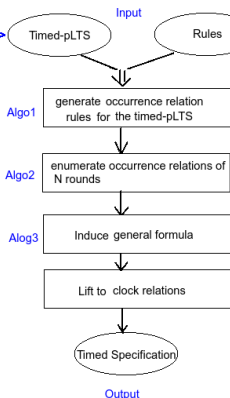
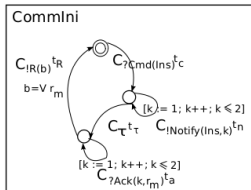
- How to generate TS of a timed-pLTS



- How to generate TS of a timed-pNets node



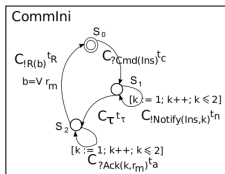
Procedure of generate TS of a Timed-pLTS



1. If $PreAct(s_0) \neq \emptyset$, then $PreAct(s_0) < PostAct(s_0)$,
 [**Assign:** $PostActIndex(s_0) \Leftarrow PreActIndex(s_0) + 1$]
2. $\forall s \setminus s_0, PreAct(s) < PostAct(s)$,
 [**Assign:** $PostActIndex(s) \Leftarrow PreActIndex(s)$];
3. $\forall s$, if $\exists \alpha. s \xrightarrow{c_\alpha} s$ and the loop executes N times, then
 - 3.1 go inside the self-loop
 - 3.1.1 from one preceding action occurrence:
 $PreAct(s) < \alpha_i$,
 - ⋮

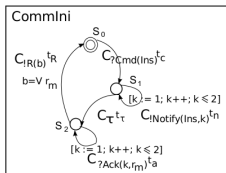
Algo1: generate concrete rules for a timed-pLTS

- Analysis causality relations on clock occurrences
- set the assignment functions according to the general rules



Transition	Action Occurrence Relations	Index Assignment
$s_2 \xrightarrow{C_{1R}} s_0 \xrightarrow{C_{7Cmd}} s_1$	$!R_m \prec ?Cmd_n$	$f(n) : n = m + 1$
$s_0 \xrightarrow{C_{7Cmd}} s_1 \xrightarrow{C_\tau} s_2$	$?Cmd_n \prec \tau_r$	$f(r) : r := n$
$s_0 \xrightarrow{C_{7Cmd}} s_1 \xrightarrow{C_{1Notify}} s_1$	$?Cmd_n \prec !notify_i$	$i := i + 1$
$s_1 \xrightarrow{C_{1Notify}} s_1 \xrightarrow{C_{1Notify}} s_1$	$!notify_i \prec !notify_i$	
$s_1 \xrightarrow{C_{1Notify}} s_1 \xrightarrow{C_\tau} s_2$	$!notify_i \prec \tau_r$	$f(r) : r = n$
s_2	$\tau_r \prec ?ack_j$	$f(j) : j = j + 1$
	$?ack_j \prec ?ack_j$	
	$?ack_j \prec !R_m$	$f(m) : m = j;$

Algo2: Travel graph



(d) pLTS

Transition	Action Occurrence Relations	Index Assignment
$s_2 \xrightarrow{C_{!R}} s_0 \xrightarrow{C_{?Cmd}} s_1$	$!R.m \prec ?Cmd.n$	$f(n) : n = m + 1$
$s_0 \xrightarrow{C_{?Cmd}} s_1 \xrightarrow{C_{\tau}} s_2$	$?Cmd.n \prec \tau.r$	$f(r) : r := n$
$s_0 \xrightarrow{C_{?Cmd}} s_1 \xrightarrow{C_{!Notify}} s_1$	$?Cmd.n \prec !notify.i$	$i := i + 1$
$s_1 \xrightarrow{C_{!Notify}} s_1 \xrightarrow{C_{!Notify}} s_1$	$!notify.i \prec !notify.(i + 1)$	
$s_1 \xrightarrow{C_{!Notify}} s_1 \xrightarrow{C_{\tau}} s_2$	$!notify.i \prec \tau.r$	$f(r) : r = n$
$s_2 \xrightarrow{C_{!R}} s_0 \xrightarrow{C_{?Cmd}} s_1$	$\tau.r \prec ?ack.j$	$f(j) : j = j + 1$
	$?ack.j \prec ?ack.(j + 1)$	
	$?ack.j \prec !R.m$	$f(m) : m = j;$

(e) rules

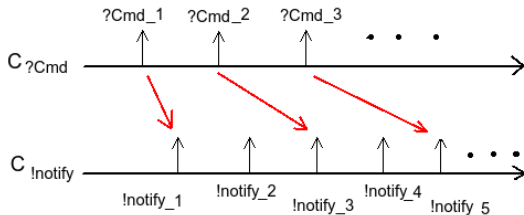
- Initial value: $m, n, r, i, j := 0; k := 1$
- reset $k := 1$ when going out self-loop

1 st round	2 nd round	3 th round	...
$!R_0 \prec ?Cmd_1$	$!R_1 \prec ?Cmd_2$	$!R_2 \prec ?Cmd_3$...
$?Cmd_1 \prec !notify_1$	$?Cmd_2 \prec !notify_3$	$?Cmd_3 \prec !notify_5$...
$!notify_1 \prec !notify_2$	$!notify_3 \prec !notify_4$	$!notify_5 \prec !notify_6$...
$!notify_2 \prec \tau_1$	$!notify_4 \prec \tau_2$	$!notify_6 \prec \tau_3$...
...			
$?ack_2 \prec !R_1$	$?ack_4 \prec !R_2$	$?ack_6 \prec !R_3$...

Algo3: Induce a general formula

- task: given a set of natural numbers, how to induce its general formula?

1 st round	2 nd round	3 th round	...	s th round
$!R_0 \prec ?Cmd_1$	$!R_1 \prec ?Cmd_2$	$!R_2 \prec ?Cmd_3$...	$!R_{(s-1)} \prec ?Cmd_s$
$?Cmd_1 \prec !notify_1$	$?Cmd_2 \prec !notify_3$	$?Cmd_3 \prec !notify_5$...	$?Cmd_s \prec !notify_{(2s-1)}$
$!notify_1 \prec !notify_2$	$!notify_3 \prec !notify_4$	$!notify_5 \prec !notify_6$...	$!notify_{(2s-1)} \prec !notify_{2s}$
$!notify_2 \prec \tau_1$	$!notify_4 \prec \tau_2$	$!notify_6 \prec \tau_3$...	$!notify_{2s} \prec \tau_s$
$\tau_1 \prec ?ack_1$	$\tau_2 \prec ?ack_3$	$\tau_3 \prec ?ack_5$...	$\tau_s \prec ?ack_{(2s-1)}$
$?ack_1 \prec ?ack_2$	$?ack_3 \prec ?ack_4$	$?ack_5 \prec ?ack_6$...	$?ack_{(2s-1)} \prec ?ack_{2s}$
$?ack_2 \prec !R_1$	$?ack_4 \prec !R_2$	$?ack_6 \prec !R_3$...	$?ack_{2s} \prec !R_s$



Lift to Clock Relations

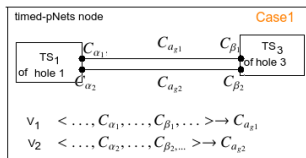
We prove: $C_\alpha^{\{P(i)\}} \prec C_\beta^{\{P'(i)\}} \Leftrightarrow \forall i \in \mathbb{N}, \alpha_-(P(i)) \prec \beta_-(P'(i))$
 so that we have :

- $C_\alpha \prec C_\beta^{\{2i-1\}} \Leftrightarrow \forall i \in \mathbb{N}, \alpha_-i \prec \beta_-(2i-1)$
- $C_\alpha^{\{2i-1\}} \prec C_\beta^{\{2i\}} \Leftrightarrow \forall i \in \mathbb{N}, \alpha_-(2i-1) \prec \beta_-(2i)$

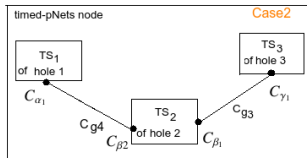
1 st round	2 nd round	...	s th round	Clock Relations
$!R_0 \prec ?Cmd_1$	$!R_1 \prec ?Cmd_2$...	$!R_{(s-1)} \prec ?Cmd_s$	$C_{!R} \prec C_{?Cmd}^{\Delta(1)}$
$?Cmd_1 \prec !notify_1$	$?Cmd_2 \prec !notify_3$...	$?Cmd_s \prec !notify_{(2s-1)}$	$C_{?Cmd} \prec C_{!notify}^{\{2s-1\}}$
$!notify_1 \prec !notify_2$	$!notify_3 \prec !notify_4$...	$!notify_{(2s-1)} \prec !notify_{2s}$	$C_{!notify}^{\{2s-1\}} \prec C_{!notify}^{\{2s\}}$
$!notify_2 \prec \tau_1$	$!notify_4 \prec \tau_2$...	$!notify_{2s} \prec \tau_s$	$C_{!notify}^{\{2s\}} \prec C_\tau$
$\tau_1 \prec ?ack_1$	$\tau_2 \prec ?ack_3$...	$\tau_s \prec ?ack_{(2s-1)}$	$C_\tau \prec C_{?ack}^{\{2s-1\}}$
$?ack_1 \prec ?ack_2$	$?ack_3 \prec ?ack_4$...	$?ack_{(2s-1)} \prec ?ack_{2s}$	$C_{?ack}^{\{2s-1\}} \prec C_{?ack}^{\{2s\}}$
$?ack_2 \prec !R_1$	$?ack_4 \prec !R_2$...	$?ack_{2s} \prec !R_s$	$C_{?ack}^{\{2s\}} \prec C_{!R}$



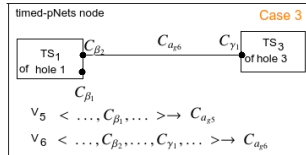
generate TS of a Timed-pNets node



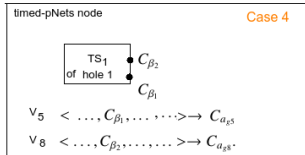
- $C_{\alpha_1} = C_{\alpha_2}$ if and only if $C_{\alpha_1} = C_{\alpha_2} \wedge C_{\beta_1} = C_{\beta_2}$.
- $C_{\alpha_1} < C_{\alpha_2}$ if and only if $C_{\alpha_1} < C_{\alpha_2} \wedge C_{\beta_1} < C_{\beta_2}$.



- $C_{\alpha_3} = C_{\alpha_4}$ if and only if $C_{\beta_1} = C_{\beta_2}$,
- $C_{\alpha_3} < C_{\alpha_4}$ if and only if $C_{\beta_1} < C_{\beta_2}$.

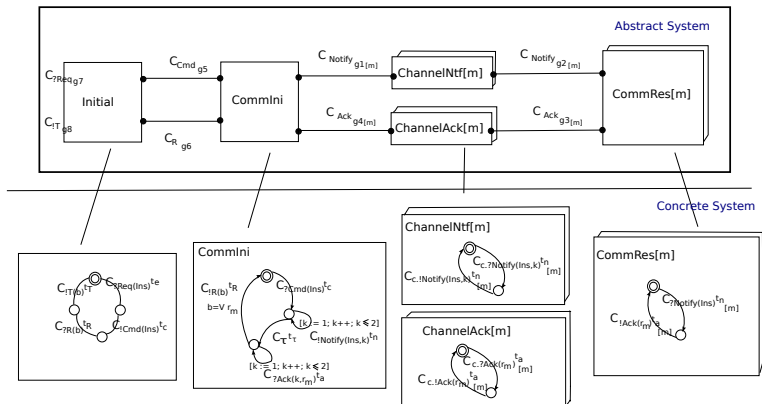


- $C_{\alpha_5} = C_{\alpha_6}$ if and only if $C_{\beta_1} = C_{\beta_2}$,
- $C_{\alpha_5} < C_{\alpha_6}$ if and only if $C_{\beta_1} < C_{\beta_2}$.



- $C_{\alpha_5} = C_{\alpha_8}$ if and only if $C_{\beta_1} = C_{\beta_2}$,
- $C_{\alpha_5} < C_{\alpha_8}$ if and only if $C_{\beta_1} < C_{\beta_2}$.

Example



- $C_{Notifyg1} \boxed{?} C_{Notifyg2} : \text{Case 2} \Rightarrow C_{Notifyg1} < C_{Notifyg2}$
- $C_{Notifyg1} \boxed{?} C_{Ackg3}$
- $C_{Cmdg5} \boxed{?} C_{Rg6} : \text{Case 1} \Rightarrow C_{Cmdg5} < C_{Rg6}$
- $C_{Cmdg5} \boxed{?} C_{?Reqg7} : \text{Case 3} \Rightarrow C_{?Reqg7} < C_{Cmdg5}$
- $C_{?Reqg7} \boxed{?} C_{!Tg8} : \text{Case 4} \Rightarrow C_{?Reqg7} < C_{!Tg8} \dots$

- Our model is flexible—logical clock, without physical common clock
- Flexible to compose by adding relations of clocks in TS

- Current work
 - Prove the completeness of the 4 cases to generating TS of timed-pNets
 - Detect wrong composition by checking the relation conflicts
- Future work
 - Extend to duration-pNets so that we can specify the actions that include execution time

- E.A. Lee. Cyber physical systems: Design challenges.
- J. Bengtsson, K. G. Larsen, F. Larsson, P. Pettersson, and Wang Yi. Uppaal a Tool Suite for Automatic Verification of Real Time Systems.
- T. Barros, R. Ameer-Boulifa, A. Cansado, L. Henrio, and E. Madelaine. Behavioural models for distributed fractal components.
- Frederic Mallet. CCSL: specifying clock constraints with UML/MARTE.
- Julien Deantoni and Frederic Mallet. TimeSquare: Treat your Models with Logical Time.

