

ILARIA CASTELLANI

CURRICULUM VITAE

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Ilaria Castellani is a permanent researcher at INRIA Sophia Antipolis since 1987. She has been working successively in the teams MEIJE (1986-1999), MIMOSA (2000-2008) and INDES (2009-today). She holds a literary baccalaureate from the Liceo Classico "Michelangiolo" of Florence (1975), an MSc (Laurea) in Computer Science from the University of Pisa (1981) and a PhD in Theoretical Computer Science from the University of Edinburgh (1988). In 1999 she obtained a habilitation for an associate professorship in Italian universities. Her main research interests are in concurrency theory and language-based security, with a recent focus on session types and reversible computation. She has co-directed the PhD theses of Ana Almeida Matos and Pejman Attar.

Ilaria Castellani is the current chair of the IFIP TC1 Working Group 1.8 on Concurrency Theory (2014-2020). She is a member of the editorial board of the French journal "Technique et Science Informatiques". She has participated in several national and European projects, as well as in bilateral projects between France and Italy, France and the UK, France and India. More recently, she took part in the European ICT Cost Action BETTY on Behavioural Types (2012-2016), where she chaired the Working Group on Security. Currently, she participates in the European ICT Cost Action IC1405 on Reversible Computation (2014-2018), and in the PICS project SuCCeSS between France and the Netherlands (2016-2019). Within INRIA, she is a member of the Committee for Gender Equality and Equal Opportunities (Comité Parité et Égalité des Chances). She also participates in the Scientific Animation Committee of the Sophia Antipolis Centre. She was the deputy leader (responsable permanente) of the MIMOSA team (2000-2008).

Main Publications

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