

Proof Obligations Preserving Compilation

Extended abstract

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Abstract. The objective of this work is to study the interaction between program verification and program compilation, and to show that the proof that a source program meets its specification can be reused to show that the corresponding compiled program meets the same specification. More concretely, we introduce a core imperative language, and a bytecode language for a stack-based abstract machine, and a non-optimizing compiler. Then we consider for both languages verification condition generators that operate on programs annotated with loop invariants and procedure specifications. In such a setting, we show that compilation preserves proof obligations, in the sense that the proof obligations generated for the source annotated program are the same that those generated for the compiled annotated program (using the same loop invariants and procedure specifications). Furthermore, we discuss the relevance of our results to Proof Carrying Code.

1 Introduction

1.1 Background and contribution

Interactive verification techniques provide a means to guarantee that programs are correct with respect to a formal specification, and are increasingly being supported by interactive verification environments that can be used to prove the correctness of safety critical or security sensitive software. For example, interactive verification environments are being used to certify the correctness of smartcard software, both for platforms and applications.

However interactive verification environments typically operate on source code programs whereas it is clearly desirable to obtain correctness guarantees for compiled programs, especially in the context of mobile code where code consumers may not have access to the source program. Therefore it seems natural to study the relation between interactive program verification and compilation.

In this paper, we focus on the interaction between compilation and verification condition generators (VC generators), which are used in many interactive verification environments to guarantee the correctness of source programs, and by several proof carrying code (PCC) architectures to check the correctness of compiled programs. Such VC generators operate on annotated programs that carry loop invariants and procedure specifications expressed as preconditions

and postconditions, and yield a set of proof obligations that must be discharged in order to establish the correctness of the program.

The main technical contribution of the paper is to show in a particular setting that compilation preserves proof obligations, in the sense that the set of proof obligations generated for an annotated source code program P is equal to the set of proof obligations generated for the corresponding annotated compiled program $\mathcal{C}(P)$ (we let $\mathcal{C}(\cdot)$ be some compilation function), where annotations for $\mathcal{C}(P)$ are directly inherited from annotations in P . The immediate practical consequence of the equivalence is that the results of interactive source program verification (i.e. the proofs that are built interactively) can be reused for checking compiled programs, and hence that it is possible to bring the benefits of interactive program verification (at source code level) to the code consumer.

One important question is whether preservation of proof obligations can be derived from the semantical correctness of the compiler (in the sense that compiled programs have the same semantics as their source counterpart), and thus can be established independently of the exact definition of the compiler. The answer is negative: our results hold for a specific compiler that does not perform any optimization, and simple program optimizations invalidate preservation of proof obligations. We return to this point in Section 5.

Another question is the choice of the source and target languages: our source and target languages are loosely inspired from Java (e.g. we handle procedure calls differently), as our main application scenario deals with Java-enabled mobile phones.

1.2 Application scenarios

In this paragraph, we propose a scenario that exploits preservation of proof obligations to bring the benefits of interactive source program verification to the code consumer. The scenario may be viewed as an instance of Proof Carrying Code (PCC) [9], from which it inherits benefits including its robustness under the code/specification being modified while transiting from producer to consumer and/or under the assumption of a malicious producer, and issues including the difficulty of expressing security policies for applications, etc.

Scenario Consider a mobile phone operator that is keen of offering its customers a new service and has the possibility to do so by deploying a program $\mathcal{C}(P)$ originating from an untrusted software company. The operator is worried about the negative impact on its business if the code is malicious or simply erroneous, and wants to be given guarantees for $\mathcal{C}(P)$. For liability reasons, the operator does not want to see the source code, and for intellectual property reasons, the software company does not want to disclose its source code nor does it authorize the operator to modify the compiled code to insert additional checks.

The equivalence of proof obligations can be used to justify the following scenario: the operator provides a partial specification of the program, e.g. a precondition ϕ and a postcondition ψ for the program **main** procedure, and requires the company to show that the program meets this partial specification. There are two possibilities: either the software company verifies directly $\mathcal{C}(P)$, which

is definitely a possibility but not the most comfortable one, or thanks to preservation of proof obligations, it can also set to verify P , and benefits from the structured nature of modern programming languages in which we assume that P has been written. To verify P , the software company suitably annotates the program, leading to an annotated program P' . Then it generates the set of proof obligations for P' and discharges each proof obligation using some verification tool that produces proofs. The compiled annotated program is sent to the operator, together with the set of proof obligations and their proofs. Upon reception, the operator checks that the compiled annotated program provided by the software company matches the partial specification it formulated in the first place (here it has to check that the precondition and postcondition are unchanged), and then run its own verification condition generator, and checks with the help of the proofs provided by the software company that these proof obligations can be discharged.

Our application scenario is being considered for specific application domains, such as midlets, where operators currently dispose of a large number of GSM applications that they do not want to distribute to their customers due to a lack of confidence in the code. Of course, we do not underestimate that our approach is costly, both by the infrastructure it requires, and by the effort involved in using it (notably by involving program verification). However, the pay-off is that our approach enables to prove precisely properties of programs, i.e. in particular correct programs will not be rejected because of some automatic method which is overly conservative (i.e. rejects correct programs).

Our approach can also be used in other mobile code scenarios. Consider for example a repository of certified algorithms; the algorithms have been written in different programming languages, but they are stored in the directory as compiled programs, e.g. as CLR programs. Prior to adding a new algorithm, say an efficient algorithm to verify square root, the maintainer of the repository asks for a certificate that the algorithm indeed computes the square root. The correctness of the algorithm must be established through interactive verification, say by the implementer of the algorithm. The implementer has the choice to write a proof using a program logic for the language in which the algorithm was developed, or using an appropriate bytecode logic. Once again, it seems likely that the first approach would be favored, and therefore that proof obligation preserving compilation would be useful.

1.3 Related work

There are several lines of work concerned with establishing a relation between source programs and compiled programs. The most established line of work is undoubtedly compiler verification [7], which aims at showing that a compiler preserves the semantics of programs.

A more recent line of work is translation validation, proposed by A. Pnueli, M. Siegel and E. Singerman [11], and credible compilation, proposed by M. Rinard [13], aim at showing for each individual run of the compiler that the resulting target program implements correctly the source program, i.e. has the same

semantics. This is achieved by the automatic generation of invariants for each program point in the source code that must be satisfied at the corresponding program points in the source code. This technique does not allow to verify that a given specification is satisfied. Related work has also been done by X. Rival [14, 15], who uses abstract interpretation techniques to infer invariants at the source level and compile these invariants for the target level.

Our work is complementary to approaches to Proof-Carrying Code based on certifying compilation. In [10], Necula and Lee propose to focus on safety properties which can be proved automatically through an extended compiler that synthesizes annotations from the information it gathers about a program, and a checker that discharges proof obligations generated by the verification condition generator. Certifying compilers are very important for the scalability of PCC, but of course the requirement of producing certificates automatically reduces the scope of properties it can handle.

There are also some recent works on program specification and verification that involve at source level and target levels: the Spec# project [3] has defined an extension of C# with annotations and type support for nullity discrimination. Such annotated programs are then compiled (with their specifications) to extended .NET files, which can be run using the .NET platform. Specifications are checked at run-time or verified using a static checker (called Boogie). This work does not consider explicitly the relationship between source and compiled program verification (but the Spec# methodology implicitly assumes some relation between the two, otherwise letting users to specify source code and have Boogie verifying the corresponding compiled program would be meaningless). In a similar line of work, L. Burdy and M. Pavlova [6] have extended the proof environment Jack, which provides a verification condition generator for JML-annotated sequential Java programs, with a verification condition generator for extended Java class files that accommodate compiled JML annotations. However, they do not establish any formal relation between the two VC generators. Independently of this work, F. Bannwart and P. Müller [2] have considered proof compilation for a substantial fragment of sequential Java, and have discuss the translation of proofs from source code to bytecode. However, their work does not discuss automatic proof verification, neither establishes the correctness of proof compilation in their setting. None of these works discusses optimizations.

For completeness, we also mention the existence of many Hoare-like logics and weakest precondition calculi for low-level languages such as the JVM or .NET or assembly languages, see e.g. [1, 5, 8, 12, 16]; many of these works have been proposed in the context of PCC.

Contents The remaining of the paper is organized as follows. Section 2 introduces syntax and annotation language, and VC generators for the assembly and source languages. Preservation of proof obligations is addressed in Section 3. Section 4 illustrates how our approach can be applied to guarantee program correctness. Finally, we conclude in Section 5 with related work and directions for future research.

2 Language and Proof Systems Definitions

In the sequel, we let \mathcal{V} be the set of values that are manipulated by programs (here $\mathcal{V} = \mathbb{Z}$), and assume given a set $\mathbb{A} \subseteq \mathcal{V} \times \mathcal{V} \rightarrow \mathcal{V}$ of arithmetic operations and a set $\mathbb{C} \subseteq \mathcal{V} \times \mathcal{V} \rightarrow \{0, 1\}$ of comparison operators. Furthermore, we assume given a set \mathcal{M} of procedure names and a set \mathcal{X} of program variables.

2.1 The assembly language

The assembly language SAL is a stack-based language with conditional and unconditional jumps, procedure calls and exceptions. It is powerful enough to compile the core imperative language described in Section 2.2.

<i>instr</i> ::=	prim <i>op</i>	primitive arithmetic operation
	push <i>n</i>	push <i>n</i> on stack
	load <i>x</i>	load value of <i>x</i> on stack
	store <i>x</i>	store top of stack in <i>x</i>
	if <i>cmp j</i>	conditional jump
	goto <i>j</i>	unconditional jump
	assert Φ	assertion Φ
	nop	no operation
	invoke <i>m</i>	procedure invocation
	throw	throw an exception
	return	end of program

where $op : \mathbb{A}$, and $cmp : \mathbb{C}$, and $x : \mathcal{X}$, and $n : \mathcal{V}$, $j : \mathbb{N}$, $m : \mathcal{M}$ and Φ is an assertion.

Fig. 1. INSTRUCTION SET

SAL programs are sets of procedures with a distinguished procedure **main**. Each procedure m consists of a function from its set \mathcal{P}_m of program points to instructions where the set of instructions is defined in Figure 1, and of a partial function $\text{Handler}_m : \mathcal{P}_m \rightarrow \mathcal{P}_m$ which specifies for each program point its handler, if any. We write $\text{Handler}_m(l) \uparrow$ if $\text{Handler}_m(l)$ is undefined, and $\text{Handler}_m(l) \downarrow$ otherwise. Program states are pairs consisting of a global register map, and a stack of frames, which correspond to the execution context of a procedure, and which consist of an operand stack, a program counter and the name of the procedure being executed. The operational semantics is standard (except for **assert** that does not change the state, i.e. it is like a no operation instruction). Note that upon a procedure invocation, a new frame is created with an empty operand stack and with the program pointer set to 1 (the initial instruction of a procedure). As to exception handling, the intuitive meaning is that if the execution at program point l in procedure m raises an exception and $\text{Handler}_m(l) = t$, then control is transferred to t with an empty operand stack. If on the contrary $\text{Handler}_m(l)$ is not defined, then the top frame is popped from the stack and the exception is transferred to the next frame.

In the sequel, we use the successor relation $\mapsto \subseteq \mathcal{P}_m \times \mathcal{P}_m$ which relates instruction to their successors. We assume that the successor of an **assert** instruction always belongs to the instructions of the procedure (we need this assumption for the sake of simplicity of definition of proof obligations further on).

Assertion language The assertion language is a standard-first order language that contains comparison between arithmetic expressions as base assertions, and is closed under conjunction and implication. One unusual feature of arithmetic expressions is that there are two special constants st and top for reasoning about the stack. The constant top represents the size of the stack in the current state, while the constant st can be thought of as an array used for an abstract representation of the operand stack. Thus we can refer to the elements of an array via expressions of the form $st(top - i)$. The set of arithmetic expressions is defined inductively as follows:

$$\begin{aligned} se & ::= top \mid se - 1 \\ aexpr & ::= n \mid x \mid aexpr \ op \ aexpr \mid st(se) \end{aligned}$$

where $op : \mathbb{A}$.

The semantics of assertions is standard, except that assertions that refer to an undefined arithmetic expression, i.e. that contain a reference to an element outside the stack bounds, are considered to be false.

The definition of the VC generator relies extensively on substitution operators. Besides the rules for substituting variables, which are standard, we also have substitution rules for top and for the non-atomic expressions, namely $st(top)$ and $top - 1$.

Well-annotated programs Verification condition generators compute from partially annotated programs a fully annotated program, in which all program points of each procedure of the program have an explicit precondition attached to them. VCGens are partial functions that require programs to be sufficiently annotated in the first place. We call such programs well-annotated.

The property of being well-annotated can be formalized through an induction principle that is reminiscent of the accessible fragment of a binary relation: that is, given a procedure P_m , a predicate R on \mathcal{P}_m , we define **ext** R inductively by the clauses: i) if $i \in R$ then $i \in \mathbf{ext} R$; ii) if for all $j \in \mathcal{P}_m$ such that $i \mapsto j$, we have $j \in \mathbf{ext} R$, then $i \in \mathbf{ext} R$. Informally, **ext** R is the set of points from which all paths eventually arrive at R .

Definition 1 (Well-annotated program).

1. Let $\mathcal{P}_m^{\text{assert}}$ and $\mathcal{P}_m^{\text{return}}$ be the set of program points i such that $P_m[i]$ is an **assert** instruction and **return** instruction respectively. Then P_m is a well-annotated procedure code iff $\mathbf{ext} (\mathcal{P}_m^{\text{assert}} \cup \mathcal{P}_m^{\text{return}}) = \mathcal{P}_m$.
2. A program is well-annotated if it comes equipped with functions $EPost : \mathcal{M} \rightarrow \text{Assn}$ and $NPost : \mathcal{M} \rightarrow \text{Assn}$ that give the exceptional and normal postcondition of each procedure, and a function $Pre : \mathcal{M} \rightarrow \text{Assn}$ which gives the precondition of a procedure, preconditions and postconditions assertions do not contain st or top , and each procedure is well-annotated.

Given a well-annotated program, one can generate a precondition for each program point. Indeed, the assertion at any given program point can be computed from the assertions for all its successors; the latter may either be given initially (as part of the partially annotated program), or have been computed previously. Note that the definition of well-annotated program does not require programs to have any particular structure, e.g. unlike [12], they do not rule out overlapping loops.

Verification condition generator The verification condition generator for assembly programs, vcg_a , is defined as a function that takes as input a well-annotated program P and returns an assertion for each program point in P . This assertion represents the weakest liberal precondition that an initial state before the execution of the corresponding program point should satisfy for the method to terminate in a state satisfying its postcondition, that is $NPost(m)$ in case of normal termination or $EPost(m)$ in case the method terminates with an unhandled exception.

The computation of vcg_a proceeds in a modular way, i.e. procedure by procedure, and uses annotations from the procedure under consideration, as well as the preconditions and post-conditions of procedures called by m . Concretely for each program point, vcg_a is defined by a case analysis on the instruction $P_m[i]$.

Its definition is given in Figure 2. Notice that we use -2 , that does not belong to the assertion language, instead of $-1 - 1$ as syntactic sugar in the definition. After calculating vcg_a of the procedure P_m (w.r.t. the annotations of P_m), we define the set of proof obligations PO_m as

$$\begin{aligned} PO_m(P_m, NPost(m), EPost(m)) &= \{\Phi_i \Rightarrow \text{vcg}_a(i+1) \mid i \in \mathcal{P}_m^{\text{assert}}\} \\ &\cup \{NPost(m') \Rightarrow \text{vcg}_a(i+1) \mid P_m[i] = \text{invoke } m'\} \\ &\cup \{Pre(m) \Rightarrow \text{vcg}_a(1)\} \cup \mathcal{M}_h(m) \cup \mathcal{M}_{\bar{h}}(m) \end{aligned}$$

where

$$\begin{aligned} \mathcal{M}_h(m) &= \{EPost(m') \Rightarrow \text{vcg}_a(t) \mid P_m[i] = \text{invoke } m' \wedge \text{Handler}_m(i) = t\} \\ \mathcal{M}_{\bar{h}}(m) &= \{EPost(m') \Rightarrow EPost(m) \mid P_m[i] = \text{invoke } m' \wedge \text{Handler}_m(i) \uparrow\} \end{aligned}$$

Proof obligations fall in one of the following categories:

- proof obligations that correspond to assertions in code;
- proof obligations triggered by procedure calls, where one has to verify that the postcondition of the invoked procedure implies the normal precondition computed for the program point that corresponds to the program point of the procedure invocation;
- the proof obligation that establishes that the normal precondition computed for the first program point follows from the procedure precondition;
- proof obligations triggered by procedure calls for the case that such calls raise an exception that is handled by the procedure m . Here one has to verify

that the exceptional postcondition of m implies the normal precondition computed for the handler of the program point where procedure invocation occurs;

- proof obligations triggered by procedure calls for the case that such calls raise an exception that is not handled by the procedure m . Here one has to verify that the exceptional postcondition of the procedure called implies the exceptional postcondition of m .

We define the set of proof obligation of a program as the union of the proof obligations of all its methods:

$$PO(P) = \bigcup_{m \in \mathcal{M}} PO_m(P_m, NPost(m), EPost(m))$$

One can prove that the verification condition generator is sound, in the sense that if the program P is called with registers set to values that verify the precondition of the procedure `main`, and P terminates normally, then the final state will verify the normal postcondition of `main`. Likewise, if P terminates abnormally, that is if an exception is thrown and there is no handler, then the final state will verify the exceptional postcondition of `main`. Soundness is proved first for one step of execution, and then extended to execution traces by induction on the length of the execution.

```

push  $n$  :    $\text{vcg}_a(i) = \text{vcg}_a(i+1)[n/st(top), top/top-1]$ 
prim  $op$  :   $\text{vcg}_a(i) = \text{vcg}_a(i+1)[st(top-1) \text{ op } st(top)/st(top), top-1/top]$ 
load  $x$  :    $\text{vcg}_a(i) = \text{vcg}_a(i+1)[x/st(top), top/top-1]$ 
store  $x$  :   $\text{vcg}_a(i) = \text{vcg}_a(i+1)[top-1/top, st(top)/x]$ 
if  $cmp\ j$  :  $\text{vcg}_a(i) = st(top-1) \text{ cmp } st(top) \Rightarrow \text{vcg}_a(i+j)[top-2/top]$ 
              $\wedge \neg(st(top-1) \text{ cmp } st(top)) \Rightarrow \text{vcg}_a(i+1)[top-2/top]$ 
goto  $j$  :    $\text{vcg}_a(i) = \text{vcg}_a(i+j)$ 
assert  $\Phi$  :  $\text{vcg}_a(i) = \Phi,$ 
nop :        $\text{vcg}_a(i) = \text{vcg}_a(i+1)$ 
throw :      $\text{vcg}_a(i) = EPost(m)$    if  $\text{Handler}_m(i) \uparrow$ 
throw :      $\text{vcg}_a(i) = \text{vcg}_a(t)$    if  $\text{Handler}_m(i) = t$ 
invoke  $m'$  :  $\text{vcg}_a(i) = Pre(m')$ 
return :     $\text{vcg}_a(i) = NPost(m)$ 

```

Fig. 2. VERIFICATION CONDITION GENERATOR FOR SAL PROCEDURES

2.2 Source language

The source language IMP is an imperative language with loops and conditionals, procedures and exceptions.

Definition 2. 1. The set AExpr of arithmetic expressions, and $\text{AProg}_{\text{IMP}}$ of commands are given by the following syntaxes:

$$\begin{aligned} \text{expr} & ::= x \mid n \mid \text{expr op expr} \\ \text{cmpexpr} & ::= \text{expr cmp expr} \\ \text{comm} & ::= \text{skip} \mid x := \text{expr} \mid \text{comm}; \text{comm} \mid \text{while } \{I\} \text{ cmpexpr do comm} \mid \\ & \quad \text{if cmpexpr then comm else comm} \mid \text{try comm catch comm} \mid \\ & \quad \text{throw} \mid \text{call } m \end{aligned}$$

where op and cmp are as in Section 2.1 and I is an assertion as defined in Section 2.1, but without the constants top and st .

2. We define a program P in IMP as a set of procedures (we use m to name a procedure), and their corresponding bodies, which are a command from $\text{AProg}_{\text{IMP}}$ (we use P_m to name a procedure code).

We define a standard verification condition generator vcg , which takes as input a command and an assertion, and returns an assertion. The function is implicitly parameterized by assertions; concretely, we assume that all procedures are annotated with a precondition, a normal postcondition, and an exceptional postcondition.

$$\begin{aligned} \text{vcg}(\text{skip}, Q, R) &= Q \\ \text{vcg}(x := e, Q, R) &= Q[e/x] \\ \text{vcg}(c_1; c_2, Q, R) &= \text{vcg}(c_1, \text{vcg}(c_2, Q, R), R) \\ \text{vcg}(\text{while } \{I\} \text{ } e \text{ do } c_1, Q, R) &= I \\ \text{vcg}(\text{if } e_1 \text{ cmp } e_2 \text{ then } c_1 \text{ else } c_2, Q, R) &= \\ & \quad (e_1 \text{ cmp } e_2) \Rightarrow \text{vcg}(c_1, Q, R) \wedge \\ & \quad \neg(e_1 \text{ cmp } e_2) \Rightarrow \text{vcg}(c_2, Q, R) \\ \text{vcg}(\text{try } c \text{ catch } c', Q, R) &= \text{vcg}(c, Q, \text{vcg}(c', Q, R)) \\ \text{vcg}(\text{throw}, Q, R) &= R \\ \text{vcg}(\text{call } m', Q, R) &= \text{Pre}(m') \end{aligned}$$

Fig. 3. VERIFICATION CONDITION GENERATOR FOR IMP PROCEDURES

We also define inductively the set PO_c of proof obligations for a command as follows:

$$\begin{aligned}
PO_c(\text{skip}, Q, R) &= \emptyset \\
PO_c(x := e, Q, R) &= \emptyset \\
PO_c(c_1; c_2, Q, R) &= PO_c(c_1, \text{vcg}(c_2, Q, R), R) \cup PO_c(c_2, Q, R) \\
PO_c(\text{while } \{I\} e \text{ do } c_1, Q, R) &= \\
& PO_c(c_1, Q, R) \cup \{I \Rightarrow (e \Rightarrow \text{vcg}(c_1, I, R) \wedge \neg e \Rightarrow Q)\} \\
PO_c(\text{if } e_1 \text{ cmp } e_2 \text{ then } c_1 \text{ else } c_2, Q, R) &= \\
& PO_c(c_1, Q, R) \cup PO_c(c_2, Q, R) \\
PO_c(\text{throw}, Q, R) &= \emptyset \\
PO_c(\text{call } m', Q, R) &= \{EPost(m') \Rightarrow R\} \cup \{NPost(m') \Rightarrow Q\} \\
PO_c(\text{try } c \text{ catch } c', Q, R) &= PO_c(c, Q, \text{vcg}(c', Q, R)) \cup PO_c(c', Q, R)
\end{aligned}$$

As in SAL, proof obligations fall in one of the following categories:

- proof obligations that correspond to annotations in while loops;
- proof obligations triggered by procedure calls,
- proof obligations triggered by procedure calls for the case that such calls raise an exception that is handled by the procedure m .

We define for every procedure m with body c , the set of proof obligations $PO_m(c, NPost(m), EPost(m))$ as:

$$\begin{aligned}
& PO_c(c, NPost(m), EPost(m)) \cup \\
& \{Pre(m) \Rightarrow \text{vcg}(c, NPost(m), EPost(m))\}
\end{aligned}$$

That is, the proof obligations of a method are those generated by the body of the methods plus the proof obligation that establishes that the precondition computed for the body of the methods follows from the procedure precondition.

Finally, the set of proof obligation for a program P is defined as the union of proof obligations for each method in P :

$$PO(P) = \bigcup_{m \in \mathcal{M}} PO_m(P_m, NPost(m), EPost(m))$$

3 Proof obligations preserving compilation

This section shows that the sets of proof obligations are preserved by a standard non-optimizing compiler. The consequence of this result is that having annotations and proofs of proof obligations for the source code, the same evidence can be used to prove automatically the correctness of its corresponding compiled program.

Definition 3. *The compilation function $C_p : \text{AProg}_{IMP} \rightarrow \text{AProg}_{SAL}$ is defined in Figure 4, using an auxiliary function $C_e : \text{AExpr} \rightarrow \text{AProg}_{SAL}$ (also defined in Figure 4), and another auxiliary function to define exception tables (defined in Figure 5).*

The compilation of exception tables defines handlers for program points of instructions enclosed in the "try" part of try-catch commands as the first program point of the code enclosed in their "catch" part.

Throughout this section, we use $\text{vcg}(p, Q, R)$ to denote both verification condition generator at source code and bytecode. For the bytecode, $\text{vcg}(p, Q, R)$ is $\text{vcg}_a(i)$ where the normal and exceptional postconditions are Q and R resp. and where i is the first program point in p .

We begin with an auxiliary lemma about expressions. Given a list P of instructions, we use the notation $P[i\dots j]$ to denote the list of instructions from instruction at i up to j .

Lemma 1. *Let e be an arithmetic expression in AExpr which appears in program P , and suppose that we have that $\mathcal{C}_e(e) = \mathcal{C}_c(P)[i\dots j]$. Let Q be an assertion in Assn that includes an arithmetic expression $st(top)$. Assume $\text{vcg}_a(j+1) = Q$. Then $\text{vcg}(i) = Q[e/st(top), top/top-1]$.*

The following lemma states that if there exists a handler c' at source level for a command c , then any exception thrown in the compilation of c will have a handler that corresponds to the compilation of c' .

Lemma 2 (Handler Preserving Compiler). *Let command $\text{try } c \text{ catch } c'$ s.t. it is the inner-most try-catch command enclosing c and let $P_m[i\dots j] = \mathcal{C}_c(c)$ and $P_m[i'\dots j'] = \mathcal{C}_c(c')$ be compilations of c and c' . Then for any $h \in \{i\dots j\}$ that can throw an exception in P_m , $\text{Handler}_m(h) = i'$ and if c is not enclosed in a try-catch command $\text{Handler}_m(h) \uparrow$.*

The following proposition establishes that compilation "commutes" with verification condition generation.

Proposition 1. $\text{vcg}(\mathcal{C}_c(c), Q, R) = \text{vcg}(c, Q, R)$

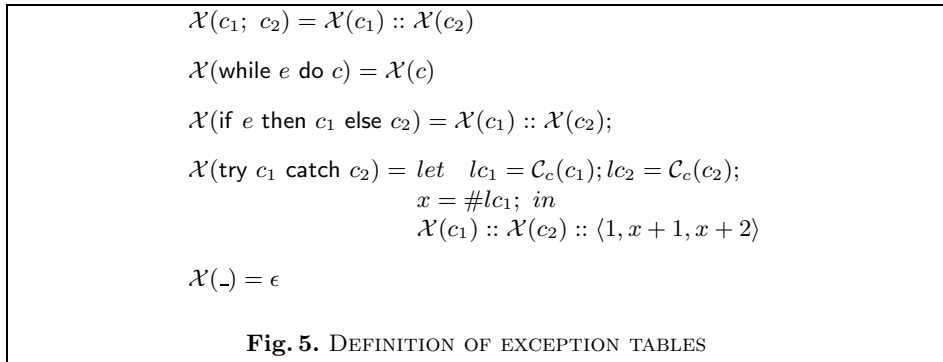
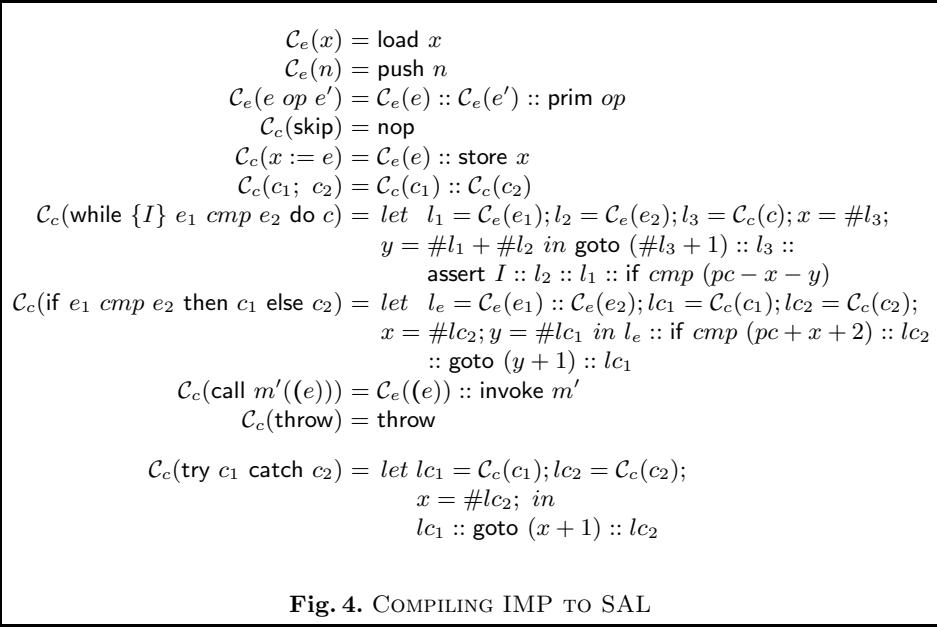
The following theorem claims that the set of proof obligations of the original program are the same of the proof obligations generated after compilation.

Theorem 1 (Proof Obligation Preserving Compilation).

$$PO_m(\mathcal{C}_c(c), Q, R) = PO_m(c, Q, R)$$

4 Example

The purpose of this section is to illustrate how the application scenario from the introduction can be applied to guarantee that compiled applications meet high-level security properties, such as the absence of uncaught exceptions, as well as specific security properties, such as non-interference; the latter is encoded in our language using self-composition as described in [4]. Here the operator will determine which program variables (in a more realistic language one would focus on method parameters) of the program P to be certified are to be considered confidential. In turn, this choice sets the precondition and the postcondition,



namely $\mathbf{x} = \mathbf{x}'$, where \mathbf{x} are the low variables of P , and \mathbf{x}' is a renaming of the low variables of P . Suppose in addition that the operator does not want the program to raise uncaught exceptions. Then the code producer must establish

$$\{\mathbf{x} = \mathbf{x}'\}P; P'\{\mathbf{x} = \mathbf{x}', \text{false}\}$$

where P' is a renaming of P with fresh variables \mathbf{x}' for low variables, and \mathbf{y}' for high-variables. False as the exceptional postcondition denotes that an exception should not be thrown. To make matter precise, consider that P is the program constituted of two procedures `main` and `aux` that take one public parameter x

```

{ $x = x'$ }verif ==  $x := y$ ; call aux;  $x' := y'$ ; call aux'
{ $x = x', false$ }

{true}
aux ==  $x := 3$ ; while { $0 \leq x$ }  $x \geq 1$  do  $y := y * x$ ;  $x := x - 1$ 
{ $x = 0, false$ }

{ $x = 0$ }
aux' ==  $x' := 3$ ; while { $0 \leq x' \wedge x = 0$ }  $x' \geq 1$  do  $y' := y' * x'$ ;  $x' := x' - 1$ 
{ $x = 0 \wedge x' = 0, false$ }

```

Proof Obligations for main:

```

 $x = x' \Rightarrow true$ 
 $false \Rightarrow false, x = 0 \Rightarrow x = 0$ 
 $false \Rightarrow false, x = 0 \wedge x' = 0 \Rightarrow x = x'$ 

```

Proof Obligations for aux:

```

 $true \Rightarrow 0 \leq 3$ 
 $0 \leq x \Rightarrow (x \geq 1 \Rightarrow 0 \leq x - 1 \wedge x < 1 \Rightarrow x = 0)$ 

```

Proof Obligations for aux':

```

 $x = 0 \Rightarrow 0 \leq 3 \wedge x = 0$ 
 $0 \leq x' \wedge x = 0 \Rightarrow (x' \geq 1 \Rightarrow 0 \leq x' - 1 \wedge x = 0 \wedge x < 1 \Rightarrow x = 0 \wedge x' = 0)$ 

```

Fig. 6. EXAMPLE: PROGRAM WITH SPECIFICATION OF NON-INTERFERENCE

and one private parameter y , with **main** and **aux** defined as

```

main ==  $x := y$ ; call aux
aux ==  $x := 3$ ; while  $x \geq 1$  do  $y := y * x$ ;  $x := x - 1$ 

```

(Note that the program is non-interfering, since it always return with $x = 0$. However, the program is typically rejected by a type system.)

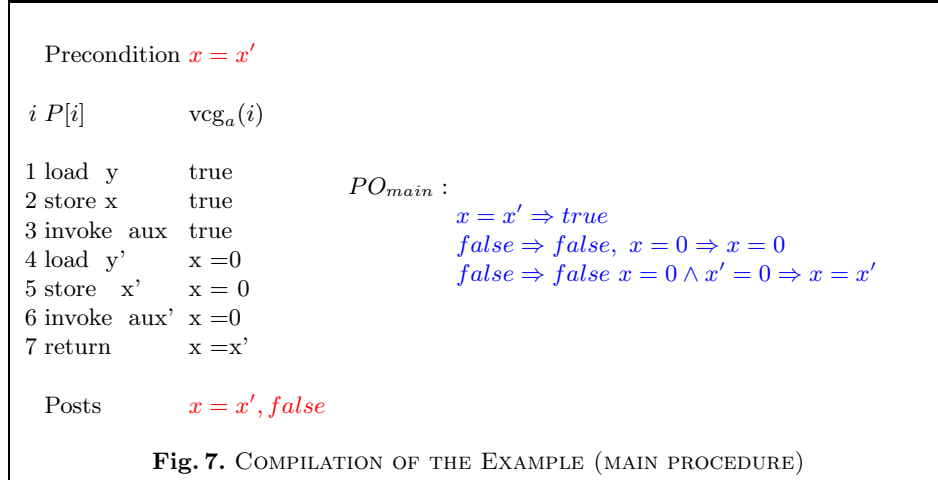
In order to prove the required properties, the software company must provide appropriate precondition and postcondition for the method **aux**, as well as appropriate loop invariants, and discharge the resulting proof obligations for the program **verif** defined as

```

verif ==  $x := y$ ; call aux;  $x' := y'$ ; call aux'

```

The annotated program is given in Figure 6, where we use red to denote the specification provided by the operator, and green to denote the specification provided by the software company. We denote with blue the set of proof obligations. In Figure 7, we show the annotated compiled program.



5 Concluding remarks

This paper shows, in a simple context, that it is possible to transfer evidence of program correctness from a source program to its compiled counterpart. Furthermore, we have shown on simple examples the possible uses of our results, and discussed some possible application domains. Although not reported here, we have also implemented a small prototype compiler and proof obligation generators to experiment our approach on small examples.

We now intend to extend our results to (non-optimizing compilers for) programming languages such as Java and C#. Furthermore, we intend to extend our results to optimizing compilers. However, preservation of proof obligations may be destroyed by simple program optimizations. If we allow optimizations, it is necessary to focus on a more general property that involves an explicit representation of proofs.

Property of Proof Compilation. For every annotated program P , a proof compiler is given by:

- a function f that gives for every proof obligation at the assembly level a corresponding proof obligation at the source level;
- a function that transforms, for every proof obligation ξ at the assembly level, proofs of $f(\xi)$ into proofs of ξ .

Proof compilation is a generalization of preservation of proof obligations and allows to bring the benefits of source code verification to code consumers. Like preservation of proof obligations, it is tied to a specific compiler; additionally, it is tied to a representation of proofs (although some degree of generality is possible here).

Preliminary investigations indicate that proof compilation is feasible for most common program optimizations. These results will be reported elsewhere.

Furthermore, we would like to explore further scenarios in which proof compilation could be used advantageously. We only mention two particularly interesting scenarios: the compilation of aspect-oriented programming, and the compilation of domain-specific languages DSLs into general purpose programs. The latter application domain seems particularly relevant since one could hope to exploit the features of DSLs to achieve easy proofs at the source code level.

Another item for future work is an evaluation of the usefulness of preservation of proof obligations and proof compilation on larger case studies. In the short term, the most promising application of our technique concerns high-level security properties that are often found in security policies for mobile applications; many of such properties are either recommended internally by the security experts to developers, or by external companies with strong security expertise (e.g. some certification authority) to solution providers (e.g. our telecom operator in the scenario of Subsection 1.2). In the longer term, it would be interesting to investigate the applicability of our method to the problem of performing dynamic updates of mobile devices infrastructures; indeed, such a scenario will probably require to establish that components behave according to their specification.

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Definition 4 (Semantics of assertions).

1. The semantics $\llbracket \cdot \rrbracket(s)$ of arithmetic expressions relative to a state s is defined inductively as follows:

$$\begin{aligned}\llbracket x \rrbracket(s) &= s \cdot \text{registers}(x) \\ \llbracket n \rrbracket(s) &= n \\ \llbracket a_1 \text{ op } a_2 \rrbracket(s) &= \llbracket a_1 \rrbracket(s) \text{ op } \llbracket a_2 \rrbracket(s) \\ \llbracket st(se) \rrbracket(s) &= \text{take}(s \cdot \text{stack}, \llbracket se \rrbracket(|s \cdot \text{stack}|))\end{aligned}$$

where, given a list l and a natural number n , $\text{take}(l, n)$ is (if it exists) the n -th element of l , and $|l|$ is the length of l , and $\llbracket se \rrbracket : \mathbb{N} \rightarrow \mathbb{N}$ is defined by the clauses:

$$\begin{aligned}\llbracket top \rrbracket(n) &= n \\ \llbracket se - 1 \rrbracket(n) &= \llbracket se \rrbracket(n) - 1\end{aligned}$$

2. The semantics $\llbracket \phi \rrbracket s$ of an assertion ϕ w.r.t. a state s is defined inductively as follows:

$$\begin{aligned}\llbracket a_1 \text{ bop } a_2 \rrbracket(s) &= \begin{cases} \mathbf{false} & \text{if } \llbracket a_1 \rrbracket(s) \text{ or } \llbracket a_2 \rrbracket(s) \text{ undefined} \\ \llbracket a_1 \rrbracket(s) \text{ bop } \llbracket a_2 \rrbracket(s) & \text{otherwise} \end{cases} \\ \llbracket true \rrbracket(s) &= \mathbf{true} \\ \llbracket false \rrbracket(s) &= \mathbf{false} \\ \llbracket \neg P \rrbracket(s) &= \neg \llbracket P \rrbracket(s) \\ \llbracket P_1 \vee P_2 \rrbracket(s) &= \llbracket P_1 \rrbracket(s) \vee \llbracket P_2 \rrbracket(s) \\ \llbracket P_1 \wedge P_2 \rrbracket(s) &= \llbracket P_1 \rrbracket(s) \wedge \llbracket P_2 \rrbracket(s) \\ \llbracket P_1 \Rightarrow P_2 \rrbracket(s) &= \llbracket P_1 \rrbracket(s) \Rightarrow \llbracket P_2 \rrbracket(s) \\ \llbracket \forall x. P \rrbracket(s) &= \forall v \in \mathbb{Z}. \llbracket P \rrbracket(s \oplus \{x \mapsto v\}) \\ \llbracket \exists x. P \rrbracket(s) &= \exists v \in \mathbb{Z}. \llbracket P \rrbracket(s \oplus \{x \mapsto v\})\end{aligned}$$

The following lemmas are used in the proof of Proposition ?? below.

Lemma 3.

$$\llbracket \varphi[n/st(top), top/top - 1] \rrbracket(\langle i, \rho, os \rangle) \Leftrightarrow \llbracket \varphi \rrbracket(\langle j, \rho, n :: os \rangle)$$

Proof. The substitution affects only assertions which refer to the stack. Assertions about stack can be made only via $stack$ and top variables ($stack(top)$ or $stack(se - 1)$), so it suffices to show that n and $stack(top)$ (top and $top - 1$) are equal under given substitution and states. For the first substitution: $\llbracket st(top) \rrbracket(\langle i, \rho, n :: os \rangle) = n = \llbracket n \rrbracket(\langle j, \rho, os \rangle)$. For the second substitution:

$$\llbracket top - 1 \rrbracket(\langle i, \rho, n :: os \rangle) = |n :: os| - 1 = |os| = \llbracket top \rrbracket(\langle j, \rho, os \rangle).$$

Lemma 4.

$$\llbracket \varphi[x/st(top), top/top - 1] \rrbracket(\langle i, \rho, os \rangle) \Leftrightarrow \llbracket \varphi \rrbracket(\langle j, \rho, \rho(x) :: os \rangle)$$

$$\begin{array}{c}
\frac{P_m[i] \in \{\text{assert } \Phi, \text{nop}\}}{\langle\langle i, os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + 1, os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{goto } j}{\langle\langle i, os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + j, os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{push } n \quad n \in \mathbb{Z}}{\langle\langle i, os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + 1, n :: os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{if } cmp \ j \quad \neg(v_1 \text{ cmp } v_2)}{\langle\langle i, v_2 :: v_1 :: os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + 1, os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{if } cmp \ j \quad v_1 \text{ cmp } v_2}{\langle\langle i, v_2 :: v_1 :: os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + j, os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{load } x}{\langle\langle i, os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + 1, \rho(x) :: os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{store } x}{\langle\langle i, v :: os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + 1, os, m \rangle :: f_s, \rho \oplus \{x \mapsto v\} \rangle} \\
\\
\frac{P_m[i] = \text{prim } op \quad n_1 \text{ op } n_2 = n}{\langle\langle i, n_2 :: n_1 :: os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i + 1, n :: os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{invoke } m'}{\langle\langle i, os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle 1, \epsilon, m' \rangle :: \langle i, os, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{return}}{\langle\langle i, os, m \rangle :: \langle i', os', m' \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle i' + 1, os', m' \rangle :: f_s, \rho \rangle} \\
\\
\frac{P_m[i] = \text{throw}}{\langle f :: f_s, \rho \rangle \rightsquigarrow \langle exc \rangle \langle f :: f_s, \rho \rangle} \\
\\
\frac{\text{Handler}_m(i) = t}{\langle exc \rangle \langle\langle i, os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle\langle t, \epsilon, m \rangle :: f_s, \rho \rangle} \\
\\
\frac{\text{Handler}_m(i) \uparrow}{\langle exc \rangle \langle\langle i, os, m \rangle :: f_s, \rho \rangle \rightsquigarrow \langle exc \rangle \langle f_s, \rho \rangle}
\end{array}$$

Fig. 8. OPERATIONAL SEMANTICS OF ASSEMBLY LANGUAGE

$$\begin{aligned}
top[a/e] &= \text{if } e = top \text{ then } a \text{ else } top \\
(st(se))[a/e] &= \text{if } e = st(top) \wedge se = top \text{ then } a \text{ else } st(se[a/e]) \\
(se - 1)[a/e] &= \text{if } e = top - 1 \wedge se = top \text{ then } a \text{ else } se[a/e] - 1
\end{aligned}$$

Fig. 9. SUBSTITUTION OPERATORS

Proof. Similar to the above, since

$$\llbracket x \rrbracket(\langle i, \rho, os \rangle) = x = \llbracket st(top) \rrbracket(\langle j, \rho, \rho(x) :: os \rangle).$$

Lemma 5.

$$\llbracket \varphi[st(top)/x, top - 1/top] \rrbracket(\langle i, \rho, n :: os \rangle) \Leftrightarrow \llbracket \varphi \rrbracket(\langle j, \rho \oplus \{x \mapsto n\}, os \rangle)$$

Proof. In φ , assertion made about the stack can be made only via top variables. Suffices to show the equality of top and $top - 1$ in the respective states (see Lemma ??). If φ is an assertion referring to x , then it suffices to show that

$$\llbracket st(top) \rrbracket(\langle i, \rho, n :: os \rangle) = n = \llbracket x \rrbracket(\langle j, \rho \oplus \{x \mapsto n\}, os \rangle)$$

Lemma 6.

$$\begin{aligned}
&\llbracket \varphi[st(top) \text{ op } st(top - 1)/st(top), top - 1/top] \rrbracket(\langle i, \rho, n_1 :: n_2 :: os \rangle) \\
&\Leftrightarrow \llbracket \varphi \rrbracket(\langle j, \rho, n_1 \text{ op } n_2 :: os \rangle)
\end{aligned}$$

Proof. Reasoning is similar as in the previous case, suffices to follow Lemma ?? and see that $\llbracket st(top) \text{ op } st(top - 1) \rrbracket(\langle i, \rho, n_1 :: n_2 :: os \rangle) = n_1 \text{ op } n_2 = \llbracket st(top) \rrbracket(\langle j, \rho, n_1 \text{ op } n_2 :: os \rangle)$

Lemma 7. $\llbracket \varphi[top - 1 - 1/top] \rrbracket(\langle i, \rho, n_1 :: n_2 :: os \rangle) \Leftrightarrow \llbracket \varphi \rrbracket(\langle j, \rho, os \rangle)$

Proof. The substitution affects only (and all) assertions referring to the stack. It suffices to show, that $top - 1 - 1$ and top are equal in their respective states:

$$\begin{aligned}
\llbracket top - 1 - 1 \rrbracket(\langle i, \rho, n_1 :: n_2 :: os \rangle) &= |n_1 :: n_2 :: os| - 2 = |os| \\
|os| &= \llbracket top \rrbracket(\langle i, \rho, os \rangle)
\end{aligned}$$

We conclude that the weakest precondition calculus is sound.

Lemma 8. *Let P be a well-annotated program and Q be its postcondition. Assume that every formula in $PO(P, Q)$ is provable.*

Then, for every states $s = \langle i, \rho, os \rangle$ and $s' = \langle j, \rho', os' \rangle$ s.t. $s \rightsquigarrow^ s'$, we have that $s \models \text{vcg}_a(i)$ implies $s' \models \text{vcg}_a(j)$.*

Proof. By induction on the length of the execution, using Lemma ?? as the base case.

Corollary 1 (Soundness of vcg_a). *Assume that all methods, except P_m satisfy their postconditions. Let P_m be a well-annotated program. Assume that every formula in PO_m is provable. Let $s = \langle \langle 1, \epsilon, m \rangle :: fs, \rho \rangle$ and $s' = \langle \langle j, os', m \rangle fs :: fs \text{ s.t. } s \rightsquigarrow^* s', \text{ and } P_m[j] = \text{return or } P_m[j] = \text{throw, then we have that } s \models \text{vcg}_a(1) \text{ implies } s' \models \text{NPost}(m) \text{ or } s' \models \text{EPost}(m) \text{ respectively.}$*

Proof. By induction in the length of the semantic steps and Proposition ??.

A Soundness

We now turn to the proof of Proposition ??.

A.1 Soundness

In the following, we let $s \models \varphi$ denote that the assertion φ holds in state s .

Proposition 2.

Let P_m be a well-annotated procedure. Assume that every formula in PO_m is provable for every procedure in the program. Then, for every states s, s' ,

- if $s = \langle \langle i, os, m \rangle :: fs, \rho \rangle$ and $s' = \langle \langle j, os', m \rangle :: fs, \rho' \rangle$, and $s \rightsquigarrow s'$, and $s \models \text{vcg}_a(i)$, then $s' \models \text{vcg}_a(j)$, furthermore if
 - if $P_m[j] = \text{return}$, then $s' \models \text{NPost}(m)$,
 - if $P_m[j] = \text{throw}$, and $\text{Handler}_m(j) \uparrow$, then $s' \models \text{EPost}(m)$,
- if $s = \langle \langle i, os, m \rangle :: fs, \rho \rangle$ and $P_m[i] = \text{invoke } m'$, and $s \models \text{vcg}_a(i)$, then $s' \models \text{Pre}(m)$, where $s' = \langle \langle 1, \epsilon, m' \rangle :: \langle i, os, m \rangle :: fs, \rho \rangle$;
- if $s' = \langle \langle j, os', m' \rangle :: \langle i, os, m \rangle :: fs, \rho' \rangle$, and $s' \rightsquigarrow s$, where $s = \langle \langle i + 1, os, m \rangle :: fs, \rho' \rangle$, and $s' \models \text{NPost}(m')$, then $s \models \text{vcg}_a(i + 1)$;
- if $s' = \langle \langle j, os', m' \rangle :: fs', \rho' \rangle$ and $s' \rightsquigarrow \langle \text{exc} \rangle s'_1 \rightsquigarrow \dots \langle \text{exc} \rangle s'_n \rightsquigarrow s$, where $s = \langle \langle t, \epsilon, m \rangle :: fs, \rho' \rangle$, and if $s' \models \text{vcg}_a(j)$ (where $\text{vcg}_a(j)$ is an assertion that does not contain top or st) if $m' = m$ or $s' \models \text{EPost}(m')$ otherwise then $s \models \text{vcg}_a(t)$.

Proof. Proof is by case analysis on each instruction.

Case: $P_m[i] = \text{push } n$. From hypothesis we know

$$s \models \text{vcg}_a(i + 1)[n/\text{st}(\text{top}), \text{top}/\text{top} - 1]$$

From semantics of $\text{push } n$ we have $s' = \langle \langle i + 1, n :: os, m \rangle :: fs, \rho \rangle$. By the substitution rules and the semantics of assertions (Lemma ??) $s' \models \text{vcg}_a(i + 1)$.

Conclusion follows if we have $j = i + 1$, $\rho' = \rho$ and $os' = n :: os$.

Case: $P_m[i] = \text{prim } op$. From hypothesis we have

$$\langle \langle i, n_1 :: n_2 :: os, m \rangle :: fs, \rho \rangle \models \text{vcg}_a(i + 1)[\text{st}(\text{top}) \text{ op } \text{st}(\text{top} - 1)/\text{st}(\text{top}), \text{top} - 1/\text{top}]$$

From semantics of $\text{prim } op$, $s' = \langle \langle i + 1, n_1 \text{ op } n_2 :: os, m \rangle :: fs, m \rangle$. By Lemma ??, $\langle \langle i + 1, \rho, n_1 \text{ op } n_2 :: os \rangle \models \text{vcg}_a(i + 1)$. We are done by picking $j = i + 1$, $\rho' = \rho$ and $os' = n_1 \text{ op } n_2 :: os$.

Case: $P_m[i] = \text{load } x$. From hypothesis,

$$s \models \text{vcg}_a(i+1)[x/\text{st}(\text{top}), \text{top}/\text{top} - 1].$$

Since $s' = \langle i+1, \rho(x) :: os, m \rangle :: fs, \rho$, from Lemma ??, we get $\langle \langle i+1, \rho(x) :: os, m \rangle, \rho \rangle \models \text{vcg}_a(i+1)$. Conclusion follows from $j = i+1$, $\rho' = \rho$ and $os' = \rho(x) :: os$.

Case: $P_m[i] = \text{if cmp } k$. Assume that $s = \langle \langle i, n_1 :: n_2 :: os, m \rangle :: fs, \rho \rangle$. From hypothesis,

$$\begin{aligned} s \models \neg(\text{st}(\text{top}) \text{ cmp } \text{st}(\text{top} - 1)) &\Rightarrow \text{vcg}_a(i+1)[\text{top} - 1 - 1/\text{top}] \\ &\wedge \text{st}(\text{top}) \text{ cmp } \text{st}(\text{top} - 1) \Rightarrow \text{vcg}_a(k)[\text{top} - 1 - 1/\text{top}] \end{aligned}$$

There are two possibilities, depending on the two top most elements on the stack: either $n_1 \text{ cmp } n_2$ holds or $n_1 \text{ cmp } n_2$ does not hold. Let's consider the first case. By semantics of assertions $\llbracket \text{st}(\text{top}) \text{ cmp } \text{st}(\text{top} - 1) \rrbracket(s)$ holds. Then from hypothesis, by conjunction elimination, and then by implication elimination, $s \models \text{vcg}_a(k)[\text{top} - 1 - 1/\text{top}]$. From semantics of $\text{if cmp } k$ and Lemma ??, we get $\langle k, \rho, os \rangle \models \text{vcg}_a(k)$. We are done if we pick $j = k$, $\rho' = \rho$ and $os' = os$. Similar for the second case.

Case: $P_m[i] = \text{store } x$. From hypothesis,

$$\langle i, n :: os, m \rangle :: fs, \rho \models \text{vcg}_a(i+1)[\text{st}(\text{top})/x, \text{top} - 1/\text{top}].$$

From semantics of $\text{store } x$, $s' = \langle i+1, \rho \oplus \{x \mapsto v\}, os \rangle$. We have, from Lemma ??, that $\langle i+1, \rho \oplus \{x \mapsto v\}, os \rangle \models \text{vcg}_a(i+1)$. Conclusion follows if $j = i+1$, $\rho' = \rho \oplus \{x \mapsto v\}$ and $os' = os$.

Case: $P_m[i] = \text{goto } j$ Trivial since goto does not change the register nor the stack.

Case: $P_m[i] = \text{assert } \Phi$. From hypothesis, we have $s \models \Phi$. From the proof obligations, we have $\Phi \Rightarrow \text{vcg}_a(i+1)$. Hence, since s' only differs from s in the program point (assert does not change registers nor stack), we get $s' \models \text{vcg}_a(i+1)$. Thus, conclusion follows from picking $j = i+1$, $\rho' = \rho$ and $os' = os$.

Case $P_m[i] = \text{invoke } m'$ By hypothesis $s \models \text{Pre}(m')$. By operational semantics $s \rightsquigarrow \langle \langle 1, \epsilon, m' \rangle :: \langle i, os, m \rangle :: fs, \rho \rangle$, Since by semantics ρ is not changed, and $\text{Pre}(m')$ is an assertion that does not include neither top or st , we have that $s' \models \text{Pre}(m')$.

Case $P_{m'}[j] = \text{return}$ By hypothesis $s' \models \text{NPost}(m')$. Since by semantics ρ is not changed, and $\text{NPost}(m')$ is an assertion that does not include neither top or st , we have that $s \models \text{NPost}(m')$ and by proof obligations $\text{NPost}(m') \Rightarrow \text{vcg}_a(i+1)$, hence $s \models \text{vcg}_a(i+1)$.

Case $s' \rightsquigarrow \langle \text{exc} \rangle s'_1 \rightsquigarrow \dots \langle \text{exc} \rangle s'_n \rightsquigarrow s$. We prove it by induction in the length of the derivation of states preceded by $\langle \text{exc} \rangle$. If $n = 1$, necessarily $P_{m'}[j] = \text{throw}$ and $m' = m$. Then $\text{vcg}_a(j) = \text{vcg}_a(t)$. Since s' and s have the same registers, $s \models \text{vcg}_a(t)$. If $2 < n$, then $\text{Handler}_{m'}(j) \uparrow$. Then $\text{vcg}_a(j) = \text{EPost}(m')$, by the proof obligation $\text{EPost}(m') \Rightarrow \text{EPost}(s'_2 \cdot \text{method})$. Since s'_2 and s' , and s'_1 have the same registers, and EPost does not contain top or st ,

then $s'_2 \models EPost(s'_2 \cdot \text{method})$ and by induction $s'_n \models EPost(s'_n \cdot \text{method})$. By proof obligations $EPost(s'_n \cdot \text{method}) \Rightarrow \text{vcg}_a(t)$. Since s'_n and s have the same registers, $s \models \text{vcg}_a(t)$.

Lemma 9. *Let e be an arithmetic expression in AExpr which appears in program P , and suppose that we have that $\mathcal{C}_e(e) = \mathcal{C}_c(P)[i\dots j]$. Let Q be an assertion in Assn that includes an arithmetic expression $st(\text{top})$. Assume $\text{vcg}_a(j+1) = Q$. Then $\text{vcg}(i) = Q[e/st(\text{top}), \text{top}/\text{top} - 1]$.*

Proof. By structural induction on expressions.

Case $e \equiv n$. Then $\mathcal{C}_c(n)[i] = \text{push } n$,

$$\begin{aligned} \text{vcg}_a(i) &= \\ Q[n/st(\text{top}), \text{top}/\text{top} - 1] \end{aligned}$$

Case $e \equiv x$.

$$\begin{aligned} \text{vcg}_a(\text{load } x) &= \\ Q[x/st(\text{top}), \text{top}/\text{top} - 1] \end{aligned}$$

Case $e \equiv e_1 \text{ op } e_2$. Let $p_1 = \mathcal{C}_e(e_1) = \mathcal{C}_c(P)[i\dots i_1]$ and let $p_2 = \mathcal{C}_e(e_2) = \mathcal{C}_c(P)[i_1 + 1\dots j - 1]$, so $\mathcal{C}_e(e) = p_1 :: p_2 :: \text{prim op}$. We have that $\text{vcg}_a(j)$ is equal to,

$$Q[st(\text{top}) \text{ op } st(\text{top} - 1)/st(\text{top}), \text{top} - 1/\text{top}]$$

By induction hypothesis, $\text{vcg}_a(i_1 + 1)$ is equal to

$$Q[\text{res}/st(\text{top}), \text{top} - 1/\text{top}][e_2/st(\text{top}), \text{top}/\text{top} - 1]$$

where $\text{res} = st(\text{top}) \text{ op } st(\text{top} - 1)$ and by induction hypothesis $\text{vcg}_a(i)$ is equal to Q' defined as:

$$Q'[e_2/st(\text{top}), \text{top}/\text{top} - 1][e_1/st(\text{top}), \text{top}/\text{top} - 1]$$

Thus, by definition of substitution,

$$\text{vcg}_a(i) = Q[e/st(\text{top}), \text{top} - 1/\text{top}]$$

Lemma 10. *Let try c' catch c' be a command in a method m , s.t. this command is the inner-most try catch enclosing c . Then the exceptional postcondition of c is $\text{vcg}(c', Q, R)$ where Q and R are postconditions for c' , and if c is not enclosed in a try-catch command, then the exceptional postcondition of c is $EPost(m)$.*

Proposition 3. $\text{vcg}(\mathcal{C}_c(c), Q, R) = \text{vcg}(c, Q, R)$

Proof. By structural induction on commands.

Case $c \equiv \text{skip}$. We have $\text{vcg}(\text{skip}, Q, R) = Q$; and $\mathcal{C}_c(\text{skip})[i] = \text{nop}$. Then, $\text{vcg}_a(i) = Q$.

Case $c \equiv x := e$. We have $\text{vcg}(x := e, Q, R) = Q[e/x]$; and

$$\mathcal{C}_c(x := e)[i \dots j, j + 1] = \mathcal{C}_e(e) :: \text{store } x$$

Then, $\text{vcg}_a(j+1) = Q[st(top)/x]$. By Lemma 1, $\text{vcg}_a(i) = Q[st(top)/x][e/st(top)]$.

Case $c \equiv c_1; c_2$. We have

$$\text{vcg}(c_1; c_2, Q, R) = \text{vcg}(c_1, \text{vcg}(c_2, Q), R)$$

and

$$\mathcal{C}_c(c_1, c_2)[i \dots j] = \mathcal{C}_c(c_1)[i \dots i_1 - 1] :: \mathcal{C}_c(c_2)[i_1 \dots j]$$

By inductive hypothesis $\text{vcg}_a(i_1) = \text{vcg}(c_2, Q, R)$ and, $\text{vcg}(i) = \text{vcg}(c_1, \text{vcg}(c_2, Q, R))$.

Case $c \equiv \text{while } \{I\} e_1 = e_2 \text{ do } c_1$. We have $\text{vcg}(c, Q, R) = I = \text{vcg}(\mathcal{C}_c(c), Q, R)$.

Case $c \equiv \text{if } e_1 \text{ cmp } e_2 \text{ then } c_1 \text{ else } c_2$. We have

$$\begin{aligned} & \text{vcg}(c, Q, R)(e_1 \text{ cmp } e_2 \Rightarrow \text{vcg}(c_1, Q, R)) \\ & \wedge (\neg(e_1 \text{ cmp } e_2) \Rightarrow \text{vcg}(c_2, Q, R)) \end{aligned}$$

By inductive hypothesis $\text{vcg}(c_i, Q, R) = \text{vcg}(\mathcal{C}_c(c_i), Q, R)$ for $i = 1, 2$.

By definition of vcg and by Lemma 1,

$$\begin{aligned} & \text{vcg}(\mathcal{C}_c(c), Q) = (e_1 \text{ cmp } e_2 \Rightarrow \text{vcg}(\mathcal{C}_c(c_1), Q, R)) \\ & \wedge (\neg(e_1 \text{ cmp } e_2) \Rightarrow \text{vcg}(\mathcal{C}_c(c_2), Q, R)) \end{aligned}$$

Case $c \equiv \text{try } c_1 \text{ catch } c_2$

$$\begin{aligned} & \text{let } lc_1 = \mathcal{C}_c(c_1); lc_2 = \mathcal{C}_c(c_2); \\ & \quad x = \#lc_2; \text{ in} \\ & \quad lc_1 :: \text{goto } (pc + x + 1) :: lc_2 \end{aligned}$$

$$\text{vcg}(\text{try } c_1 \text{ catch } c_2, Q, R) = \text{vcg}(c_1, Q, \text{vcg}(c_2, Q, R))$$

By IH $\text{vcg}(c_2, Q, R)$ is equal to $R' \equiv \text{vcg}_a(pc + \#lc_1 + 1)$ and again by IH $\text{vcg}(c_1, Q, R') = \text{vcg}(lc_1, Q, R')$.

Case $c \equiv \text{throw}$ $\text{vcg}(\text{throw}, Q, R) = R$ and if $P[pc] = \text{throw}$ is its compilation, then either $\text{vcg}_a(pc) = \text{EPost}(m)$ (i.e. R) if there is no handler for pc or $\text{vcg}_a(pc) = \text{vcg}_a(t)$ if $\text{Handler}(pc) = t$.

The former case is straightforward. For the second, notice that by Lemma 2, in the source program throw is enclosed in a try-catch and the first instruction of the compilation of the handler (call it c') is t . By the rule of try-catch, the second assertion in vcg is exactly the vc of c' , and by IH it is equal to $\text{vcg}_a(t)$.

Case $c \equiv \text{call } m$ Its compilation is $\text{invoke } m'$. The equivalence is straightforward by definition of vcg .

This covers all the cases.

Proposition 4 (Proof Obligation Preserving Compilation).

$$PO_m(\mathcal{C}_c(c), Q, R) = PO_m(c, Q, R)$$

Proof. By structural induction on commands, using Lemma 1.

Case $c \equiv \text{skip}$. We have $PO_m(c, Q, R) = \emptyset$ and $\mathcal{C}_c(\text{skip})[i] = \text{nop}$, and $PO_m(\text{nop}, Q, R) = \emptyset$.

Case $c \equiv x := e$. We have $PO_m(x := e, Q) = \emptyset$ and $\mathcal{C}_c(x := e)[i \dots j, j + 1] = \mathcal{C}_e(e) :: \text{store } x$, and by Lemma 1 and PO_m for **store**. By Lemma 1, $PO_m = \emptyset$.

Case $c \equiv c_1; c_2$. Straightforward from inductive hypothesis.

Case $c \equiv \text{while } \{I\} \ e_1 \ \text{cmp} \ e_2 \ \text{do} \ c_1$. We have

$$PO_m(c, Q) = PO_m(c_1, Q) \cup \{I \Rightarrow (e_1 \ \text{cmp} \ e_2 \Rightarrow \text{vcg}(c_1, I) \wedge \neg(e_1 \ \text{cmp} \ e_2) \Rightarrow Q)\}$$

$$\begin{aligned} \mathcal{C}_c(c) = \text{let } & l_1 = \mathcal{C}_e(e_1); l_2 = \mathcal{C}_e(e_2); l_3 = \mathcal{C}_c(c_1); x = \#l_3; \\ & y = \#l_1 + \#l_2 \ \text{in} \ \text{goto} \ (pc + x + 1) :: l_3 :: \\ & \text{assert } I :: l_2 :: l_1 :: \text{if } \text{cmp} \ (pc - x - y) \end{aligned}$$

By inductive hypothesis $PO_m(c_1, I) = PO_m(l_3, I)$ and by Lemma 1, the set of proof obligations of arithmetic expressions is empty. The proof obligation generated by the **assert** I instruction in the code above is:

$$I \Rightarrow \text{vcg}(l_2 :: l_1 :: \text{if } \text{cmp} \ (pc - x - y), Q)$$

where $\text{vcg}(l_2 :: l_1 :: \text{if } \text{cmp} \ (pc - x - y), Q) = (e_1 \ \text{cmp} \ e_2 \Rightarrow \text{vcg}_a(pc - x - y) \wedge \neg(e_1 \ \text{cmp} \ e_2) \Rightarrow Q)$ where $\text{vcg}_a(pc - x - y) = \text{vcg}(c_1, I)$, by Proposition 1.

Case $c \equiv \text{if } e_1 \ \text{cmp} \ e_2 \ \text{then} \ c_1 \ \text{else} \ c_2$ Straightforward by inductive hypothesis and Lemma 1.

Case $c \equiv \text{try } c_1 \ \text{catch} \ c_2$

$$PO_m(\text{try } c_1 \ \text{catch} \ c_2, Q, R) = PO_m(c_1, Q, R) \cup PO_m(c_2, Q, R)$$

The equivalence holds by inductive hypothesis.

Case $c \equiv \text{throw } e$ $PO_m(\text{throw}, Q) = \emptyset$ and $PO_m(\mathcal{C}_c(\text{throw } e), Q, R) = \emptyset$.

Case $c \equiv \text{call } m'$ $PO_m(\text{call } m', Q, R) = \{EPost(m') \Rightarrow R\} \cup \{NPost(m) \Rightarrow Q\}$. The compilation of c is **invoke** m' . There are two cases, the **invoke** m' instruction is handled in m , and one proof obligation is: $EPost(m') \Rightarrow \text{vcg}_a(\text{Handler}_m(i))$ or the instruction is not handled in m and the proof obligation is $EPost(m') \Rightarrow EPost(m)$. Depending on the cases, both right hand sides of the implication are equivalent to R , since according to the definition of vcg , R represents exactly the vc of the handler in the try-catch if there is a try-catch; otherwise it is $EPost(m)$. The other proof obligation for the **invoke** instruction is for the normal case: $NPost(m') \Rightarrow \text{vcg}_a(i + 1)$, where $i + 1$ is the successor instruction of **invoke**. But $\text{vcg}_a(i + 1)$ is exactly the vc of the successors of c and by Lemma 1, $\text{vcg}_a(i + 1) = Q$.