

C2S@Exa September, Paris

Parallel Hierarchical Hybrid Algebraic Linear Solvers : current and future

HiePACS Inria Project Joint Inria-CERFACS lab INRIA Bordeaux Sud-Ouest

Motivations



The "spectrum" of linear algebra solvers

Direct

- Robust/accurate for general problems
- BLAS-3 based implementations
- Memory/CPU prohibitive for large 3D problems
- ★ Limited parallel scalability

Iterative

- Problem dependent efficiency/controlled accuracy
- Only mat-vect required, fine grain computation
- Less memory computation, possible trade-off with CPU
- ★ Attractive "build-in" parallel features

Goal: design Hybrid Linear Solvers

Develop robust scalable parallel hybrid direct/iterative linear solvers

- * Exploit the efficiency and robustness of the sparse direct solvers
- Develop robust parallel preconditioners for iterative solvers
- Take advantage of the natural scalable parallel implementation of iterative solvers

Domain Decomposition (DD)

- Natural approach for PDE's
- Extend to general sparse matrices
- Partition the problem into subdomains, subgraphs
- Use a direct solver on the subdomains
- * Robust preconditioned iterative solver



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Two Hybrid Linear Solvers

Algebraic non-overlapping domain decomposition

- * Partitioning of the adjacency graph of the sparse matrix
- Perform a partial Gaussian elimination (sparse direct solution on the internal variables)
- Solve the Schur complement system using a preconditioned Krylov subspace method
- Backsolve for the internal variables

Two parallel implementations

- HIPS (Hierarchical Iterative Parallel Solver) Incomplete LU factorization of the Schur complement based on a hierarchical interface decomposition ordering
- MaPHyS (Massively Parallel Hybrid Solver) Algebraic additive Schwarz preconditioner for the Schur complement

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Based on a non-overlapping domain decomposition



- B : Interior nodes of subdomains (direct factorization).
- C : Interface nodes.

Special decomposition and ordering of the subset C : Goal : Building a global Schur complement preconditioner (ILU) from the local domain matrices only.



HIPS: domain interface based fill-in policy

P.Hénon, Y. Saad - SIAM SISC 06] [J.Gaidamour, P.Hénon -IEEE CSE 08]

Special decomposition and ordering of the subset C :

Hierachical interface decomposition into connectors :







Rules :

- ★ No creation of edge (fill-in) outside the local domain matrices.
- Allow edges between connectors adjacent to the same subdomain.
- ⇒ keep the parallelism (communication only between adjacent subdomains).

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Robust block incomplete factorization of the Schur complement

- * Hierachy of separators (wirebasket like faces , edges, vertices)
- Block incomplete factorization with "geometrical" fill-in policy to express parallelism (Global factorization using only local sub-domain matrices)
- MIS ordering to express parallelism within incomplete factorisation steps

HIPS: numerical and parallel features

- Memory control via treshold parameters (interior, Schur approximation and incomplete factorization)
- Parallel load balancing: mapping of several subdomains on each MPI process

main

MaPHyS: Algebraic Additive Schwarz preconditioner

L.Carvalho, L.G., G.Meurant- NLAA - 01]

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 $\mathcal{S} = \sum_{i=1}^{N} \mathcal{R}_{\Gamma_i}^T \mathcal{S}^{(i)} \mathcal{R}_{\Gamma_i}$ $S = \begin{pmatrix} \ddots & & \\ & S_{kk} & S_{k\ell} & \\ & S_{\ell k} & S_{\ell \ell} & S_{\ell m} \\ & & S_{m\ell} & S_{mm} & S_{nn} \\ & & S_{nm} & S_{nn} \end{pmatrix} \Longrightarrow \mathcal{M} = \begin{pmatrix} \ddots & & \\ & S_{kk} & S_{k\ell} & -1 \\ & S_{\ell k} & S_{\ell \ell} & S_{\ell m} & -1 \\ & & S_{\ell \ell} & S_{\ell m} & S_{nn} \\ & & & S_{nm} & S_{nn} \end{pmatrix}$ Snm S_{nn} $\mathcal{M} = \sum_{i=1}^{N} \mathcal{R}_{\Gamma_i}^T (\bar{\mathcal{S}}^{(i)})^{-1} \mathcal{R}_{\Gamma_i}$ where $\bar{S}^{(i)}$ is obtained from $S^{(i)}$ Similarity with Neumann-Neumann preconditioner [J.F $\mathcal{S}^{(i)} = \begin{pmatrix} \mathcal{S}_{kk}^{(\iota)} & \mathcal{S}_{k\ell} \\ \mathcal{S}_{\ell k} & \mathcal{S}_{\ell \ell}^{(\iota)} \end{pmatrix} \Longrightarrow \overline{\mathcal{S}}^{(i)} = \begin{pmatrix} \mathcal{S}_{kk} & \mathcal{S}_{k\ell} \\ \mathcal{S}_{\ell k} & \mathcal{S}_{\ell \ell} \end{pmatrix}$ Bourgat, R. Glowinski, P. Le Tallec and M. Vidrascu - 89] [Y.H. de Roek, P. Le Tallec and M. Vidrascu - 91] local Schur local assembled Schur $\iota \in adj$

Ongoing/future software development

- ★ Improved software flexibility (rely on MUMPS, PastiX, Scotch, ...)
- Hybrid MPI-Thread implementation (PhD thesis DIP Inria/Total) on top of PaStiX
- * Implementation on top of runtime systems cf Emmanuel's talk

Ongoing/future numerical development

- Improved numerical robustness hierarchical toward global preconditioning (ADT Maphys@Exa via C2S@Exa)
- * Coarse space mechanisms through augmentation and/or deflation
- New Krylov subspace methods (block variants, hiden/avoiding communications, ...) - shared with sparse directs (cf Emmanuel's talk)

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